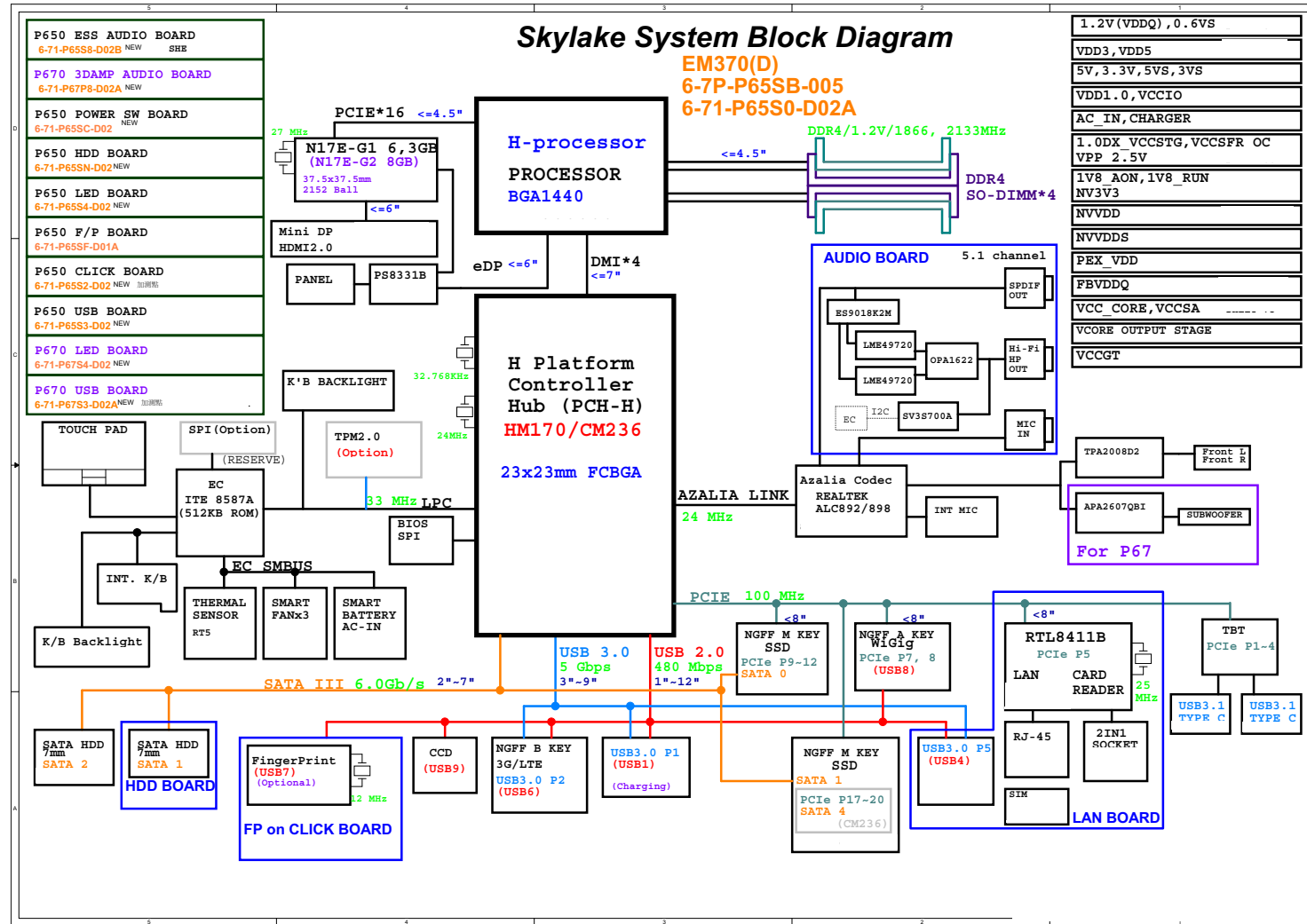
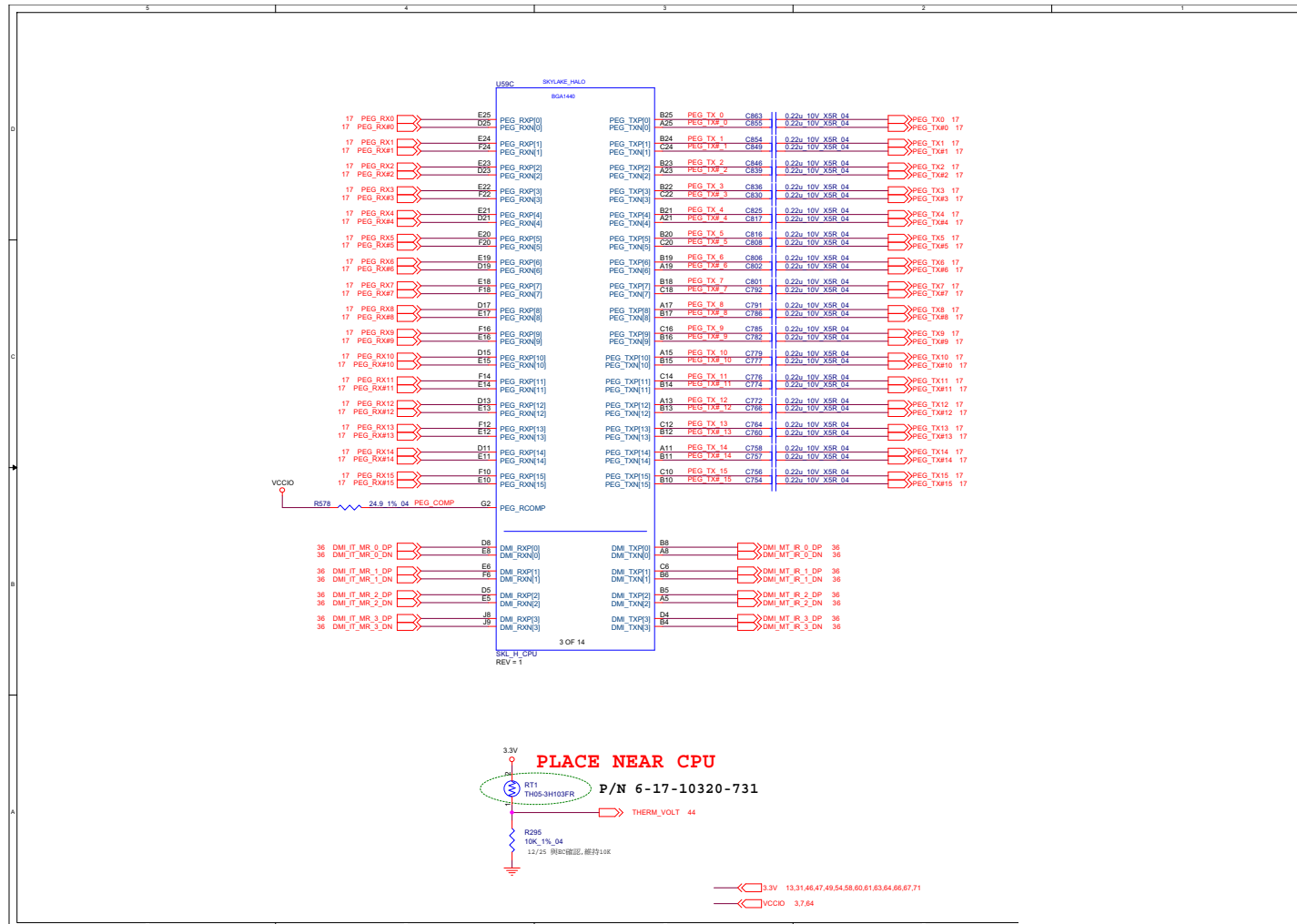


System Block Diagram



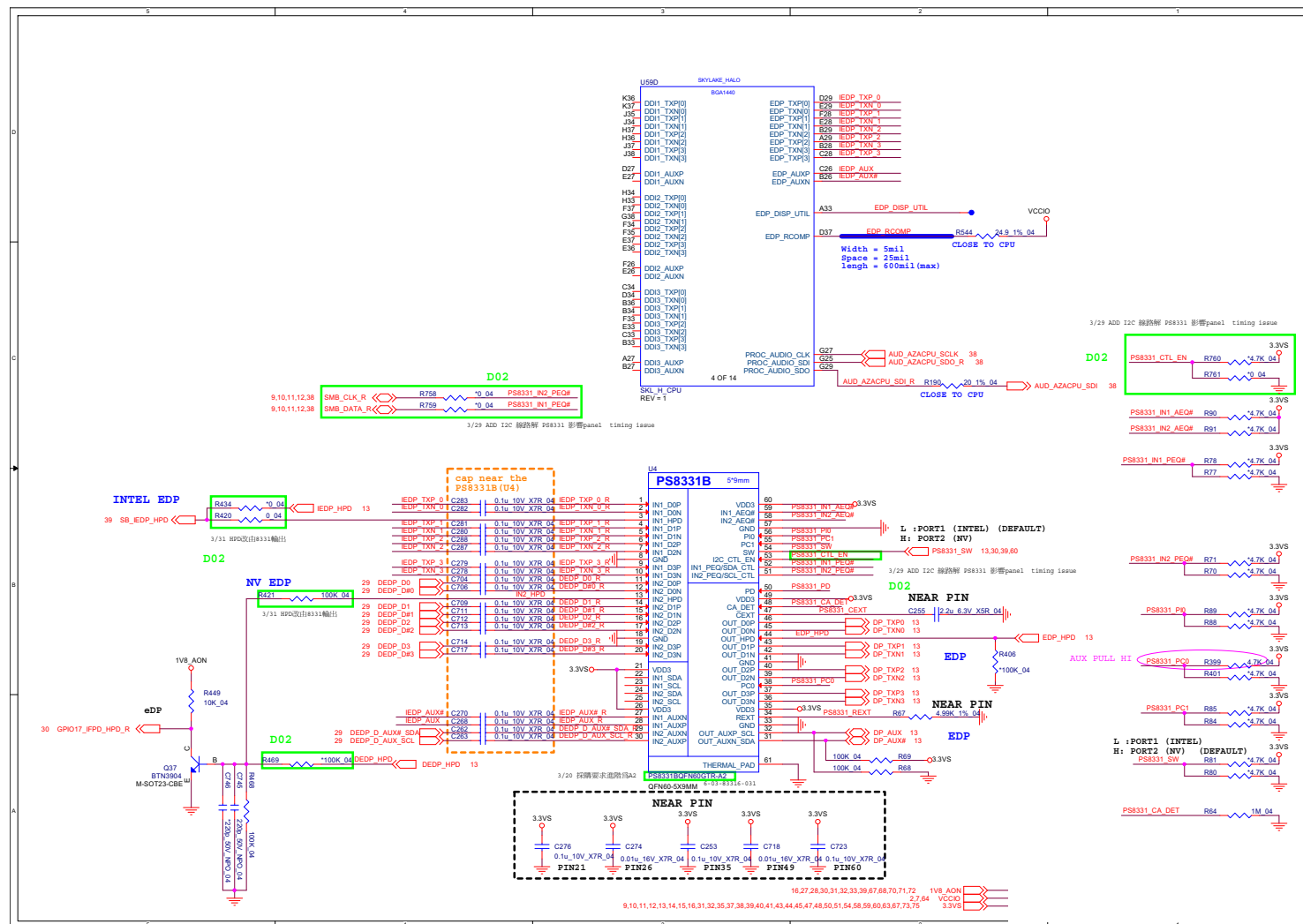
Sheet 1 of 91
System Block
Diagram

Processor 1/7

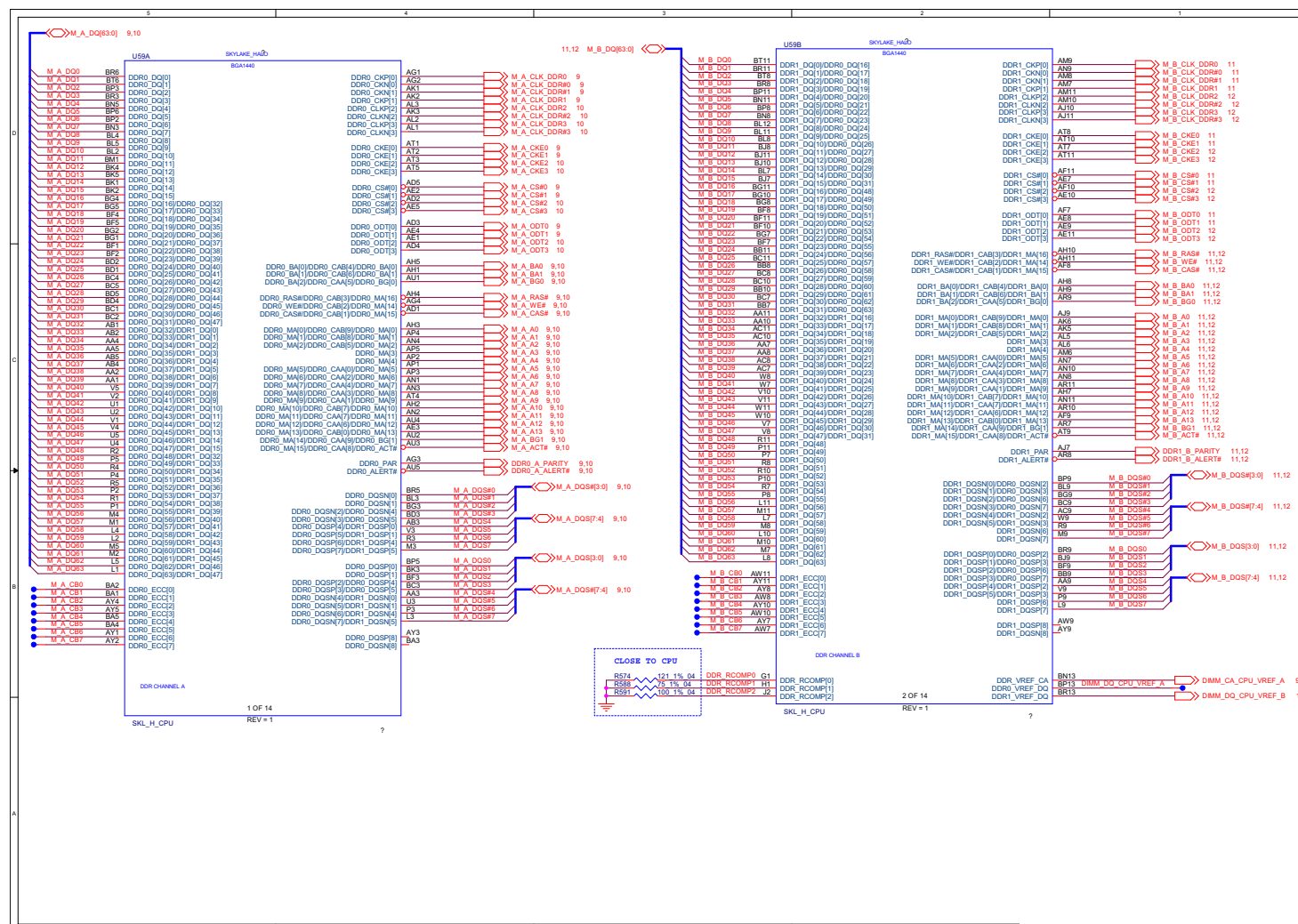
Sheet 2 of 91
Processor 1/7

Processor 2/7

Sheet 3 of 91
Processor 2/7



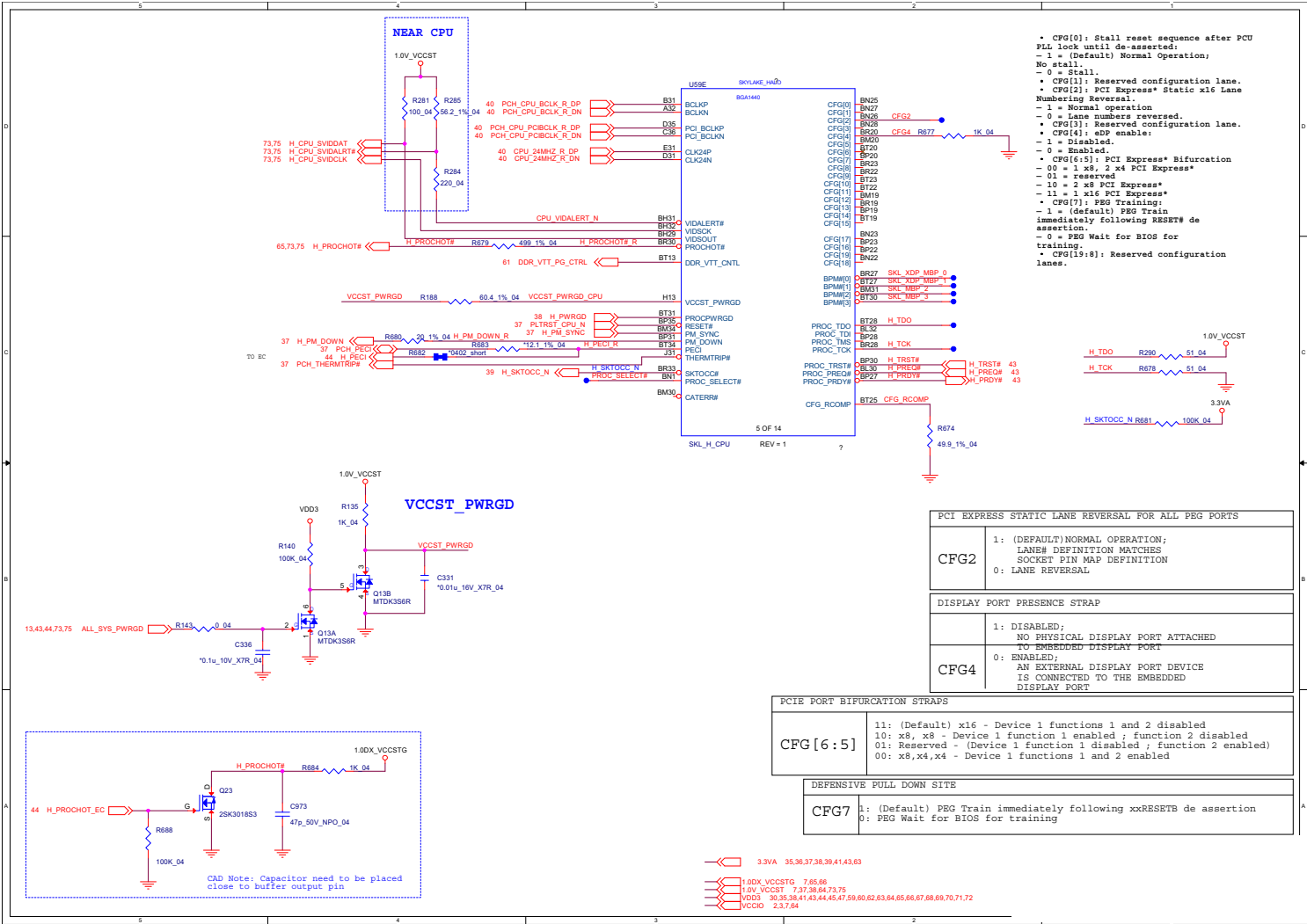
Processor 3/7 B - 5



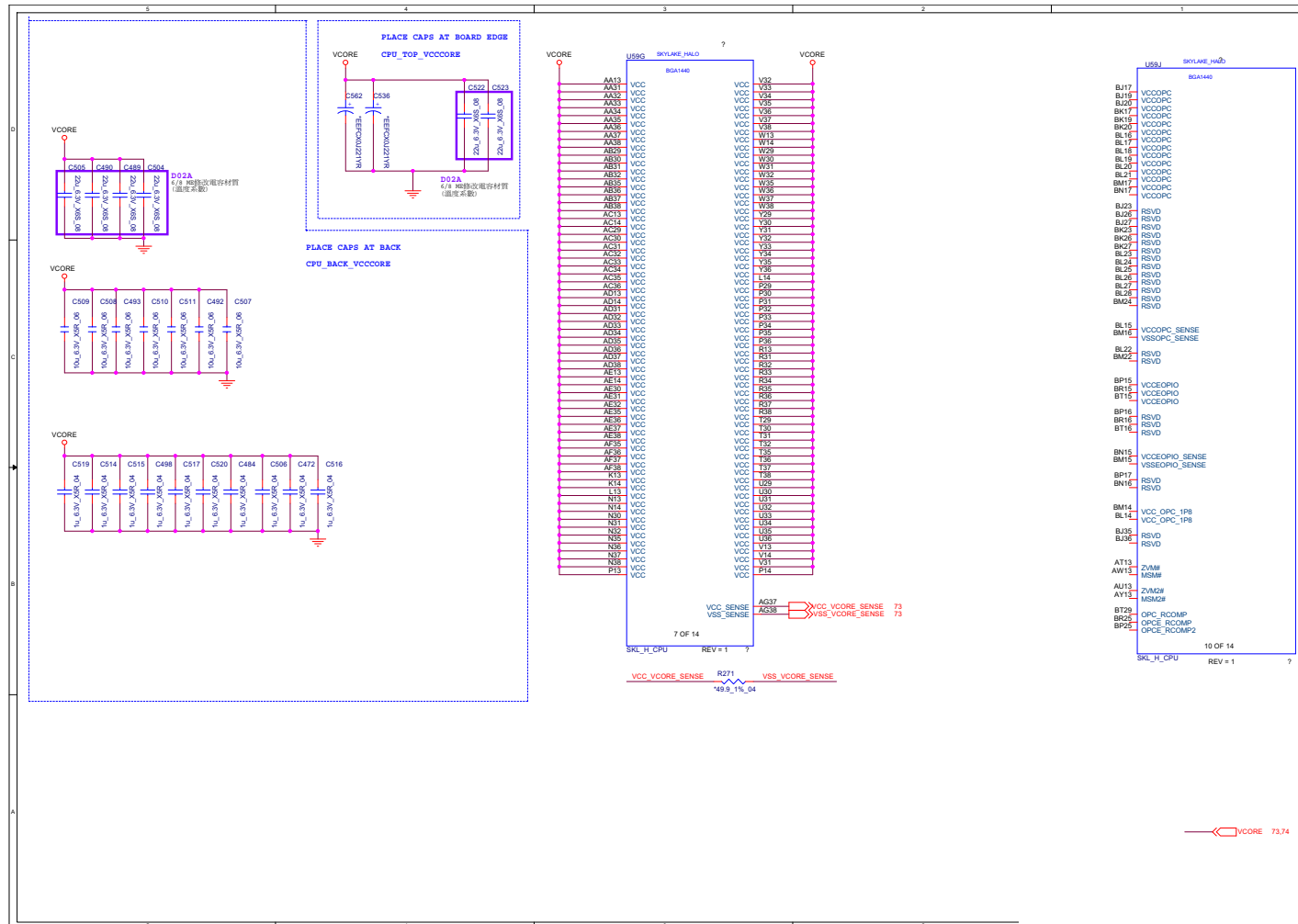
Schematic Diagrams

Processor 4/7

Sheet 5 of 91
Processor 4/7

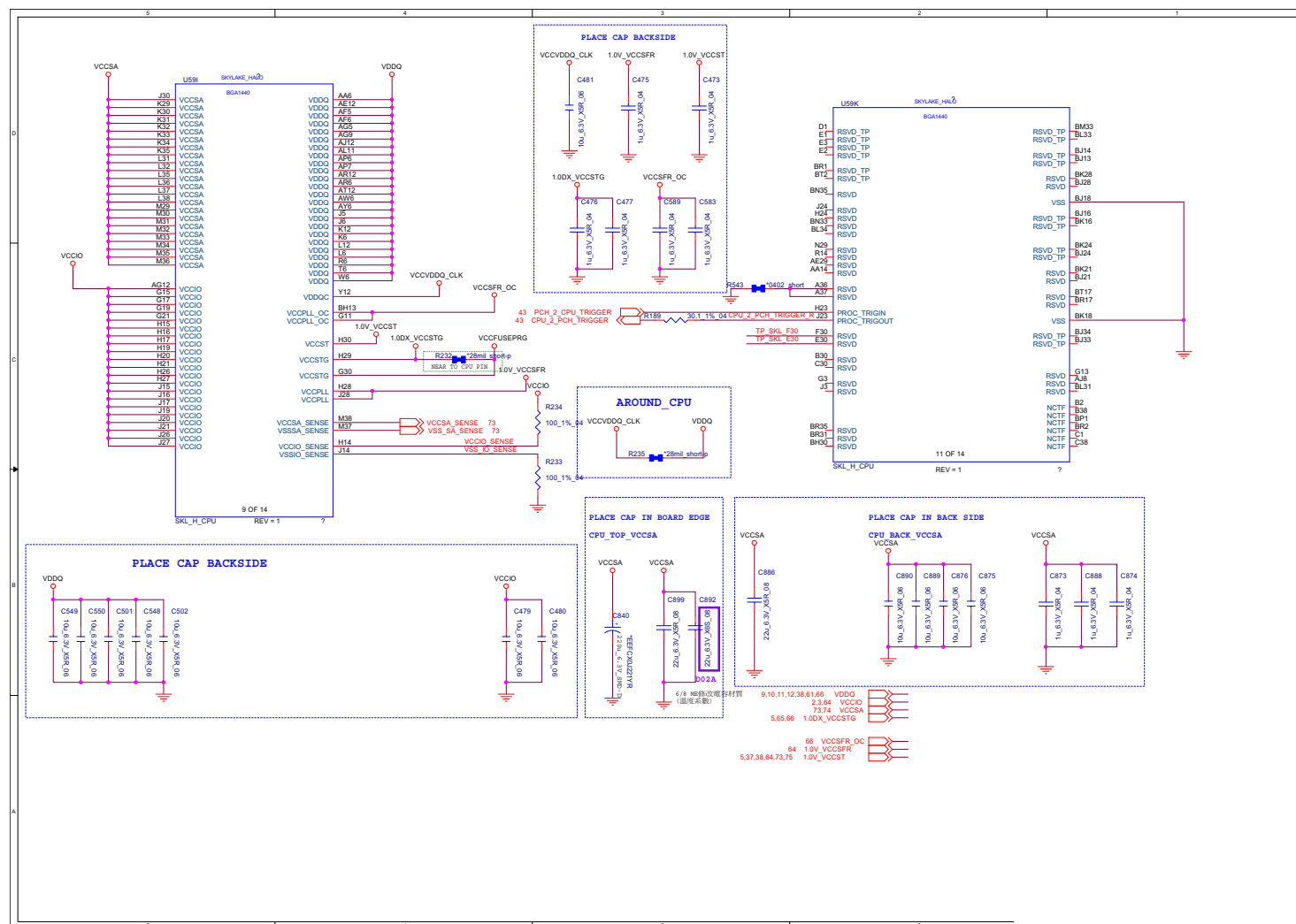


Processor 5/7

Sheet 6 of 91
Processor 5/7

Processor 6/7

Sheet 7 of 91
Processor 6/7



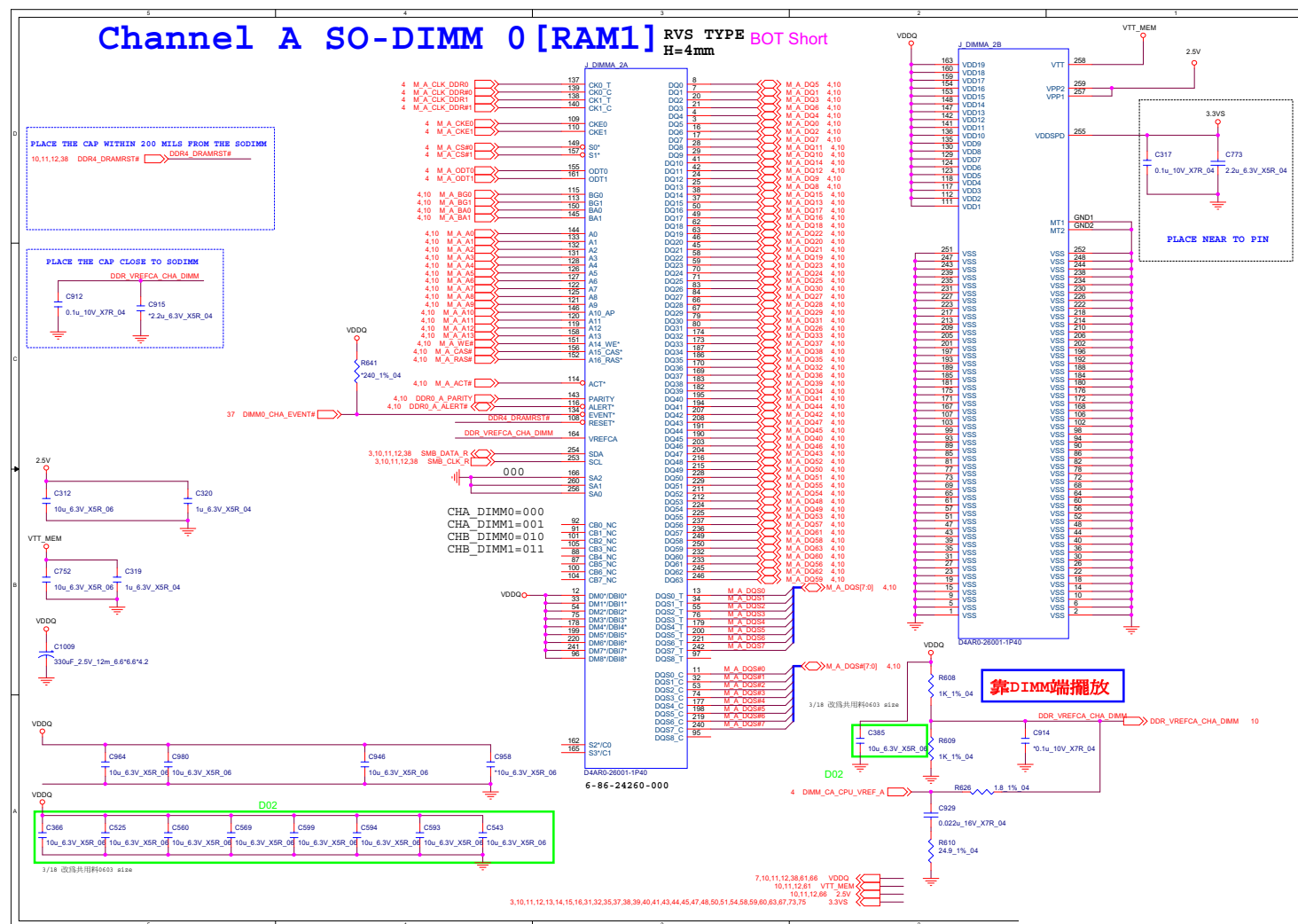
Processor 7/7 B - 9

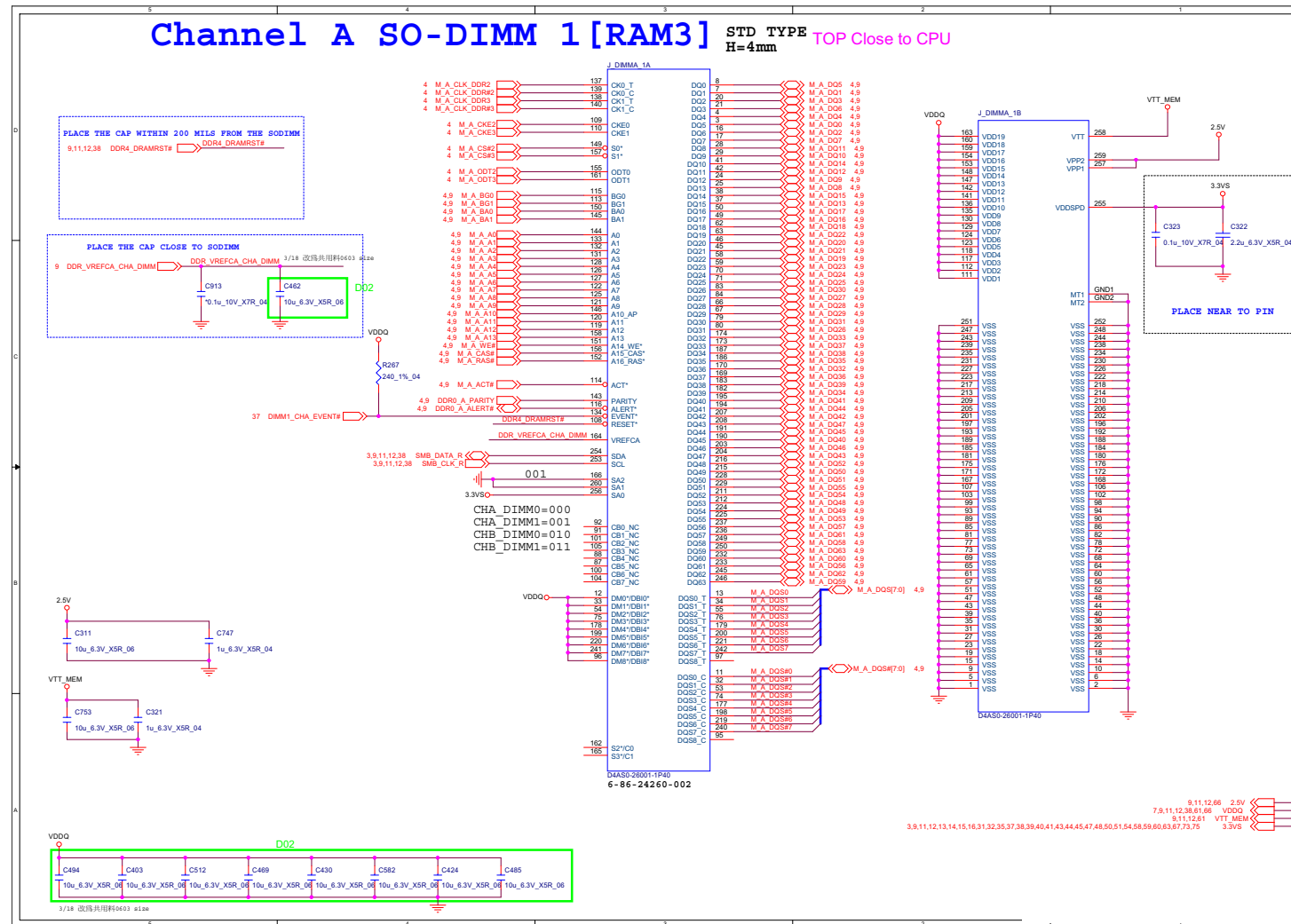
Sheet 8 of 91
Processor 7/7



DDR CHA SO-DIMM_0

Sheet 9 of 91
DDR CHA SO-
DIMM 0



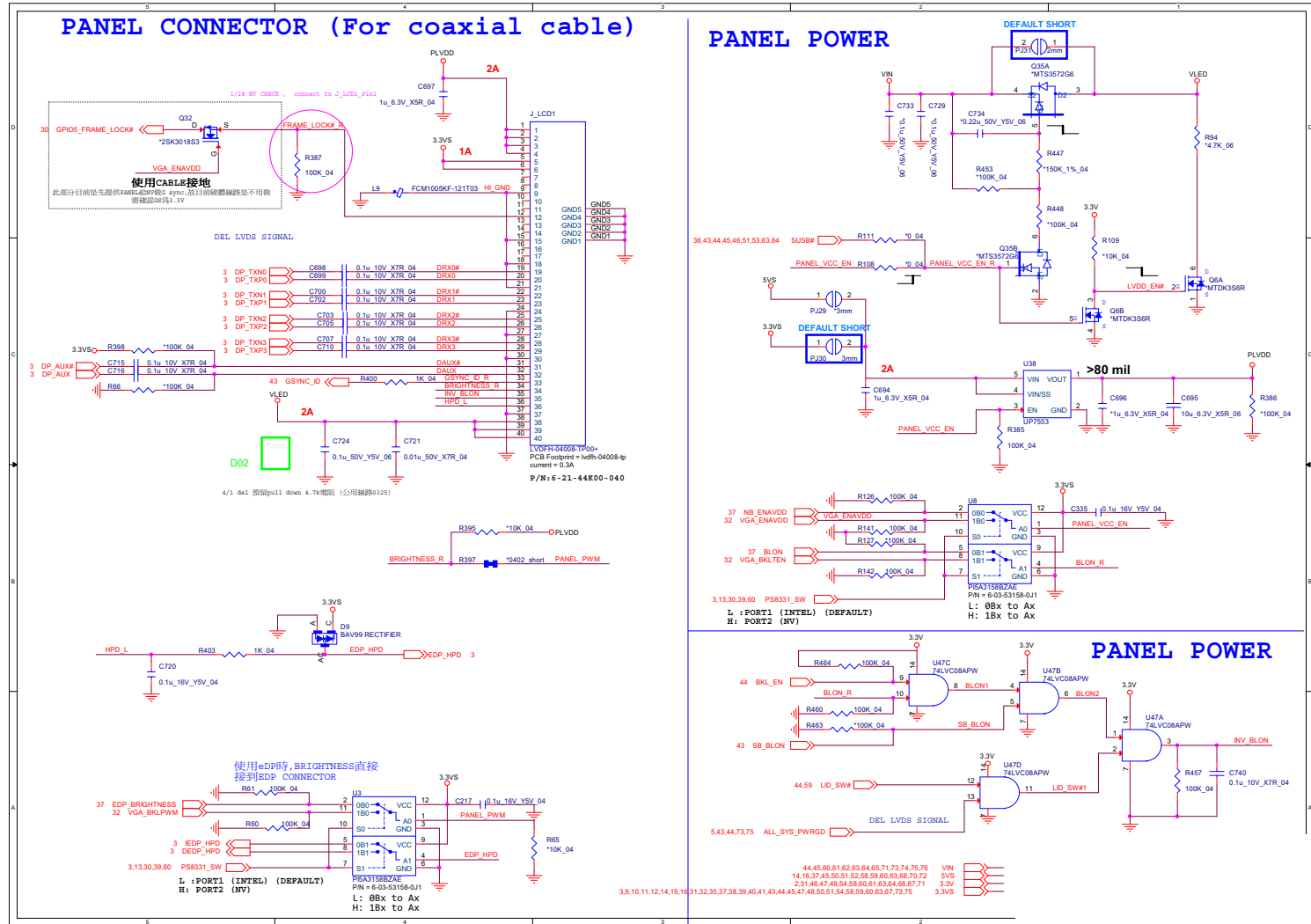


B.Schematic Diagrams

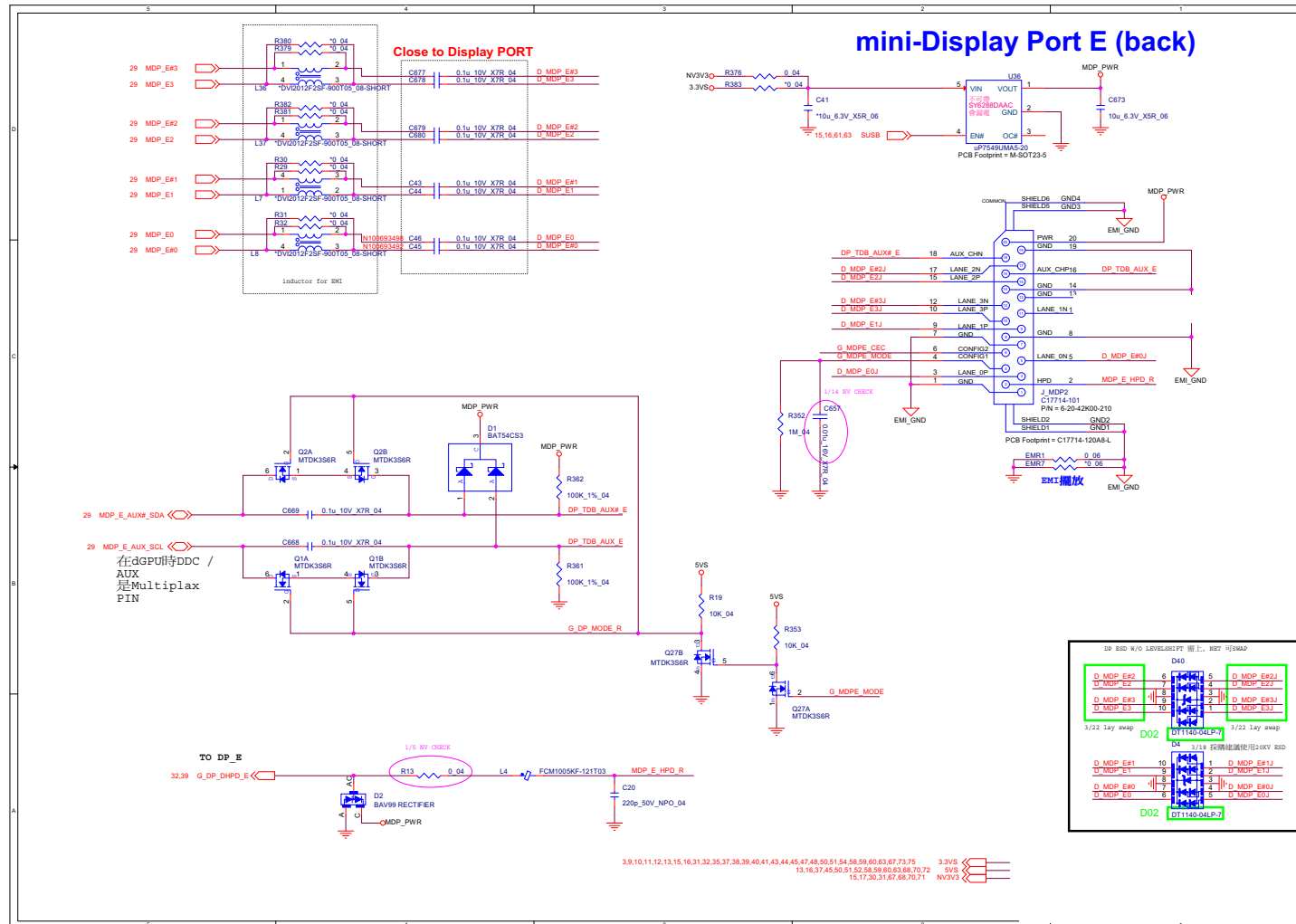
Sheet 10 of 91
DDR CHA SO-
DIMM_1

Panel, Inverter

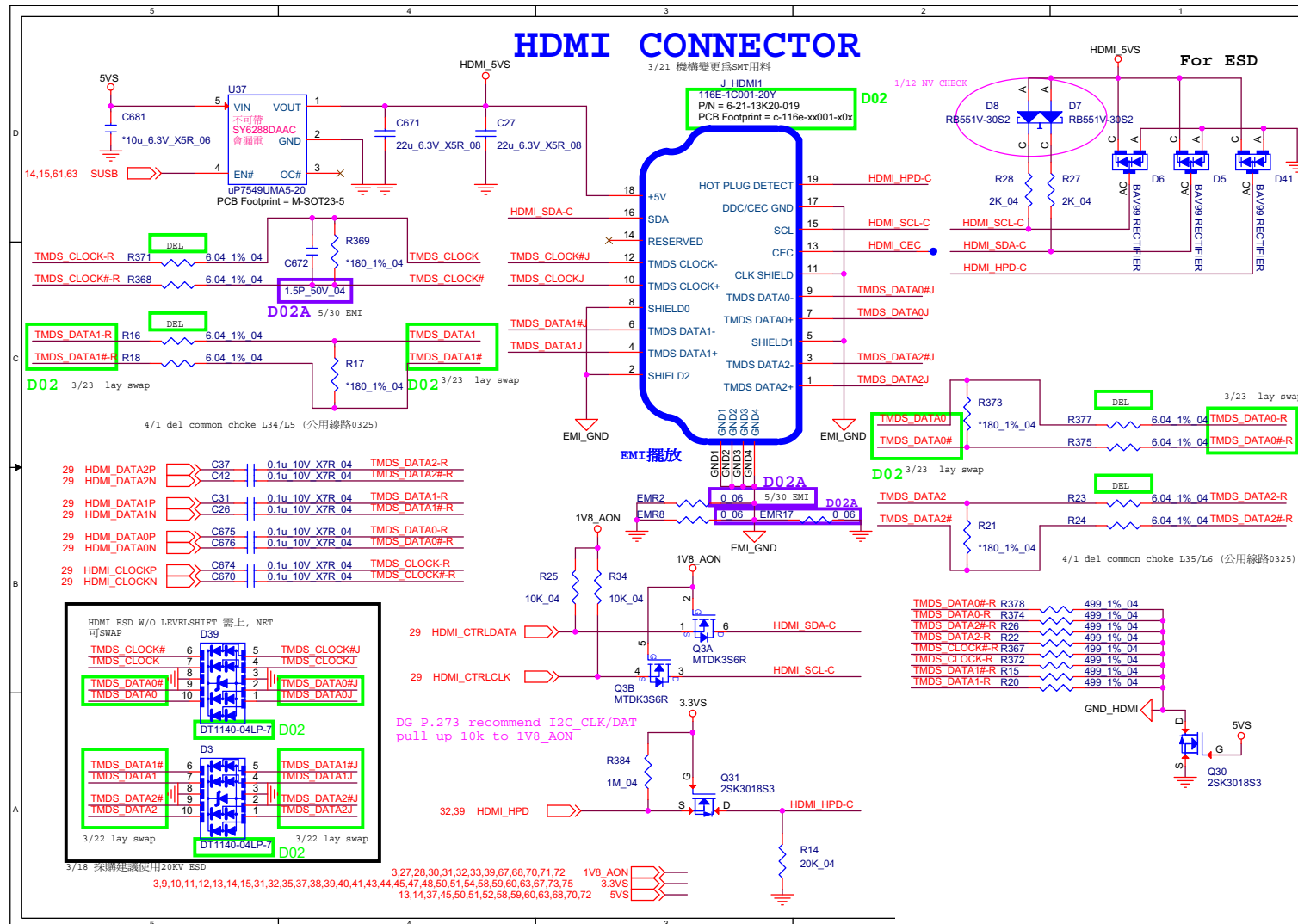
Sheet 13 of 91
Panel, Inverter



Mini DP Port E B - 15



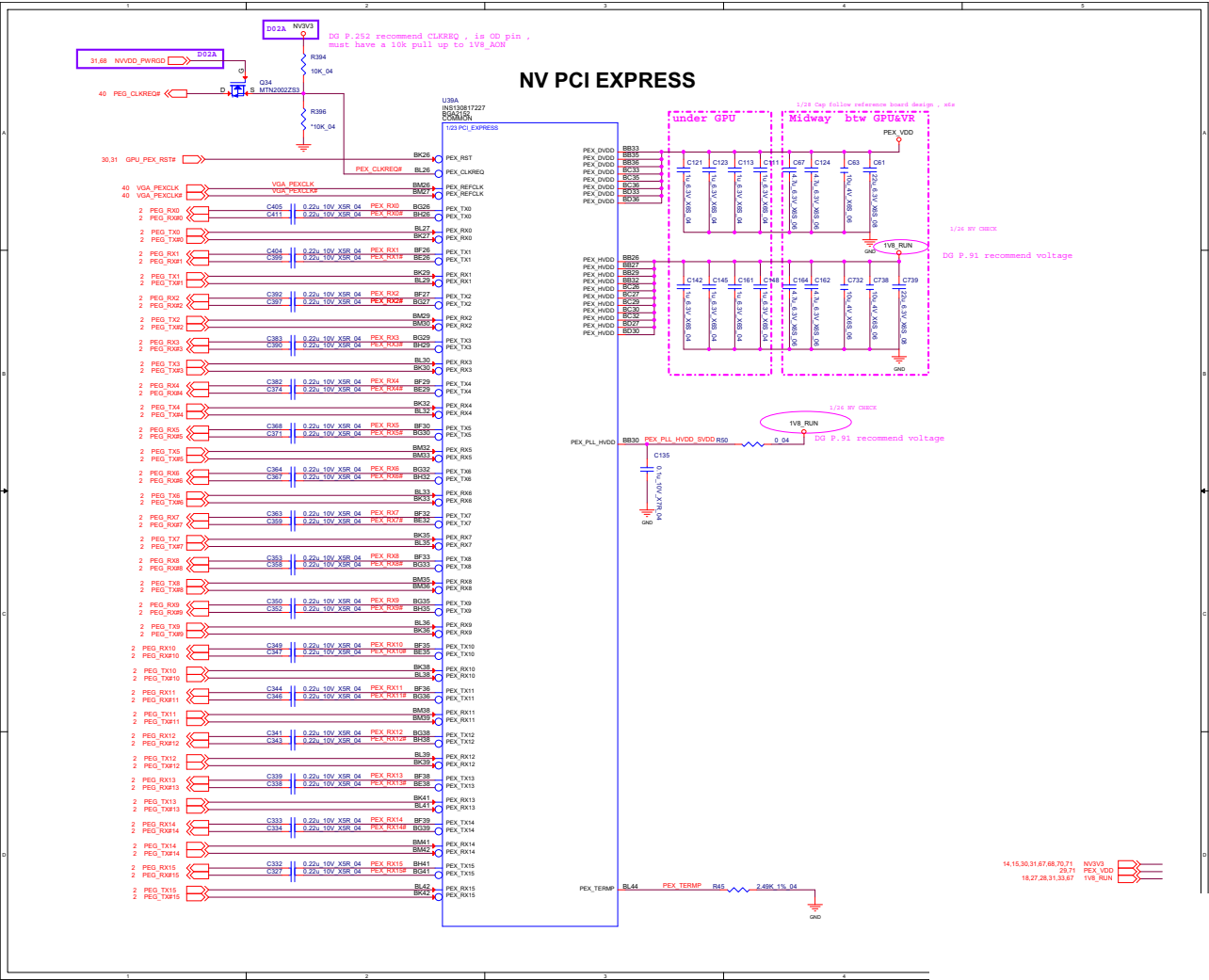
HDMI B - 17



Schematic Diagrams

VGA PCI Express

Sheet 17 of 91
VGA PCI Express

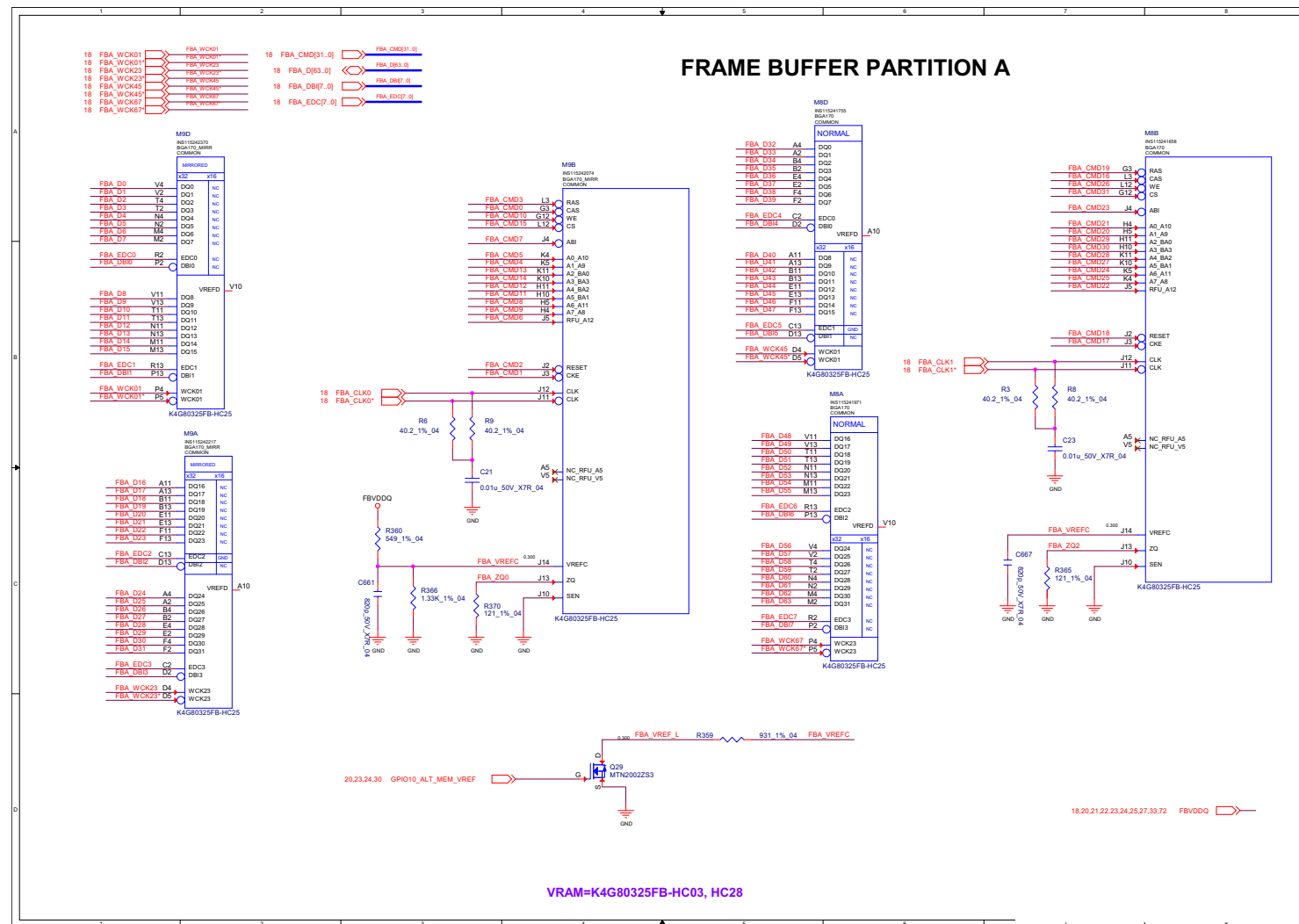


Buffer Partition B - 19



Frame Buffer Partition A

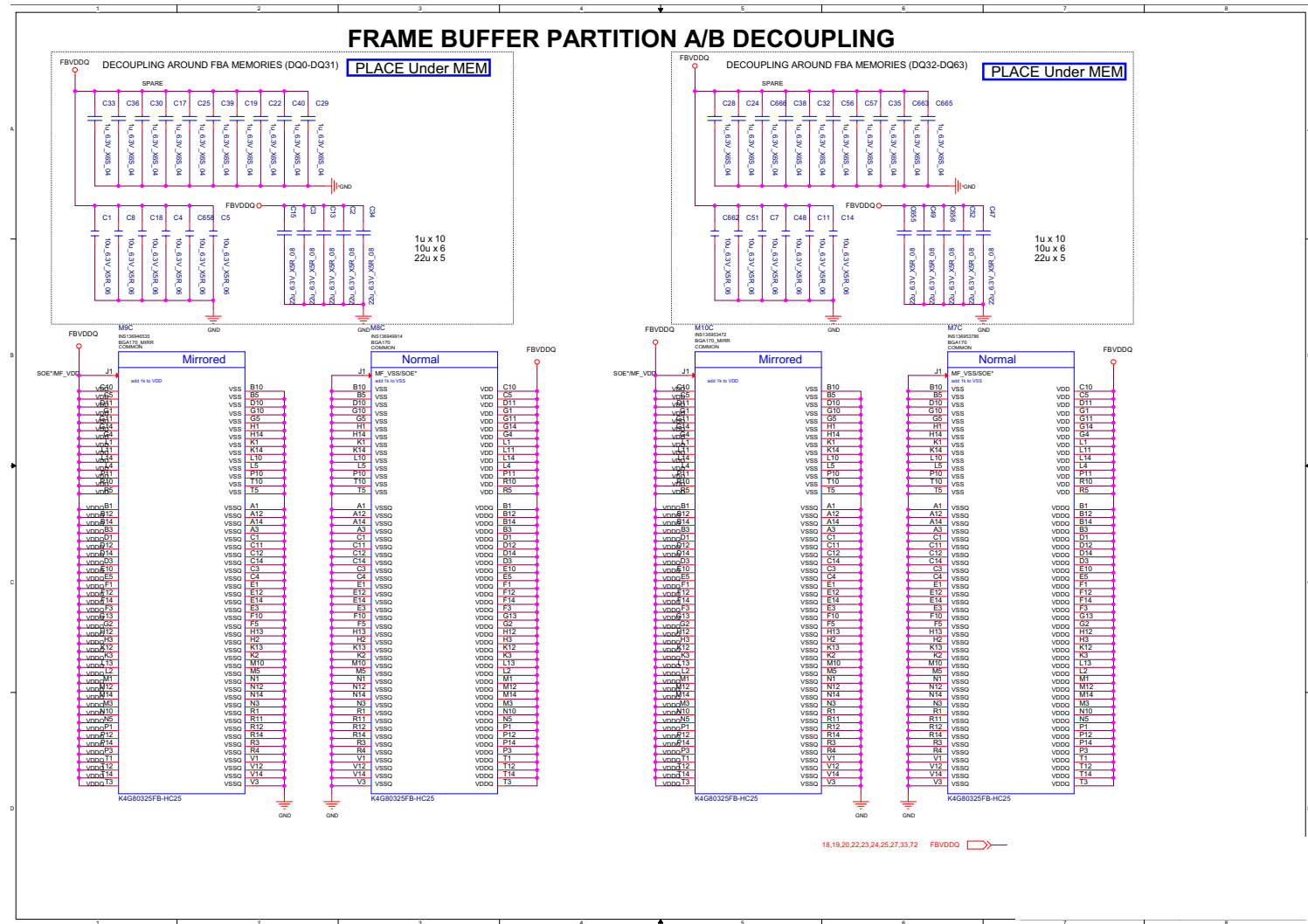
Sheet 19 of 91
Frame Buffer
Partition A



Sheet 20 of 91
Frame Buffer
Partition B

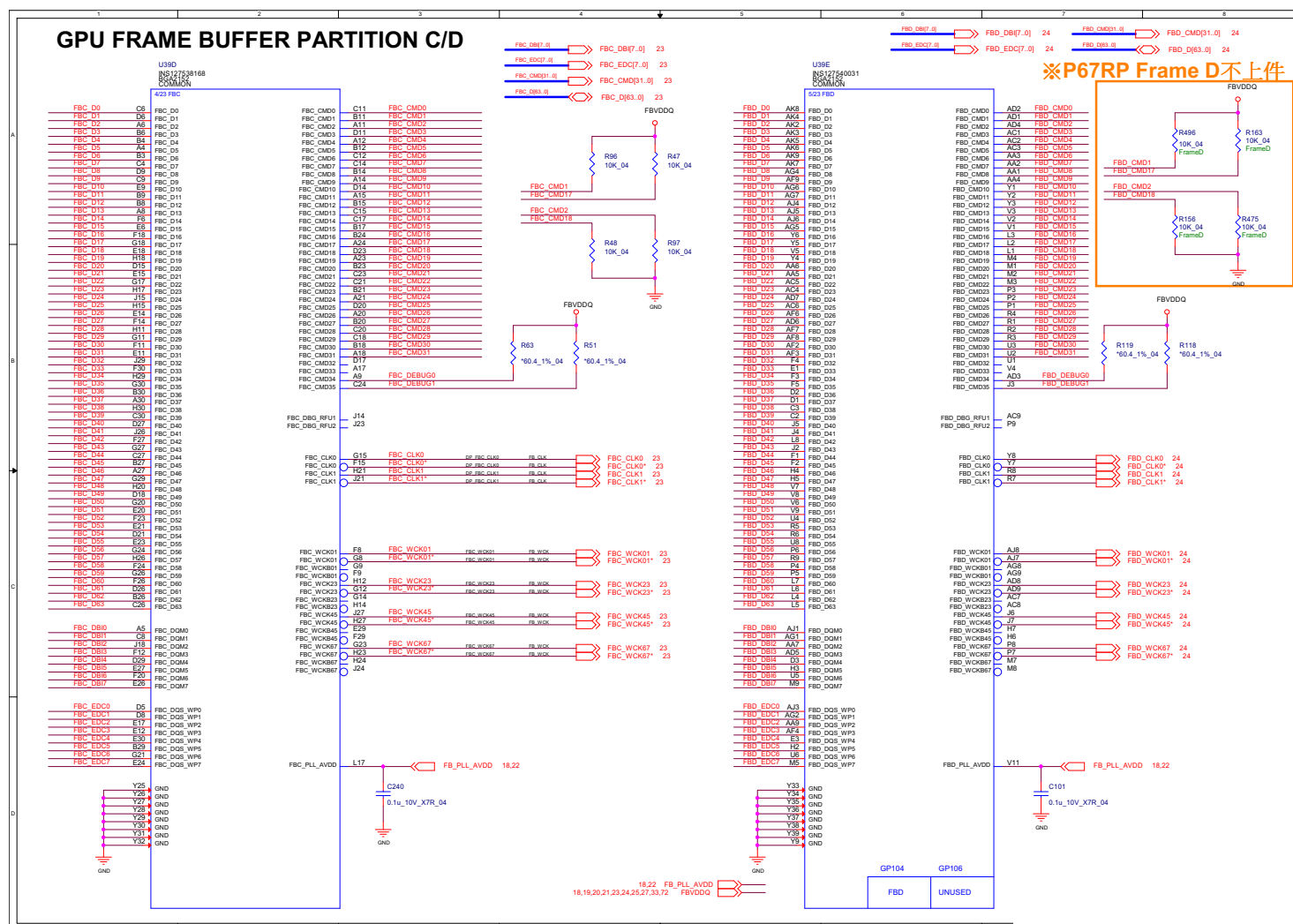


Frame Buffer Partition A_B

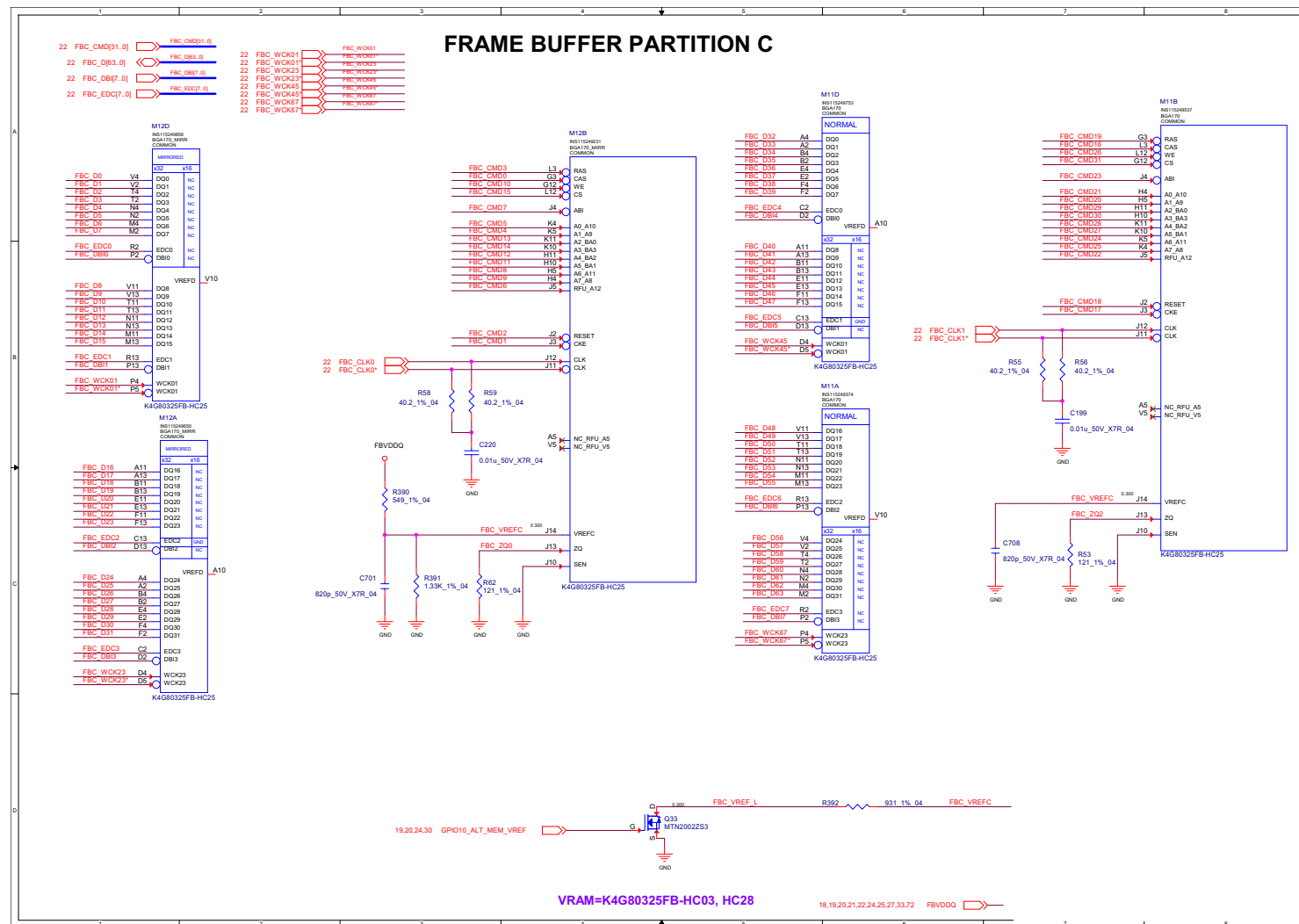


Sheet 21 of 91
Frame Buffer
Partition A_B

GPU Frame Buffer Partition

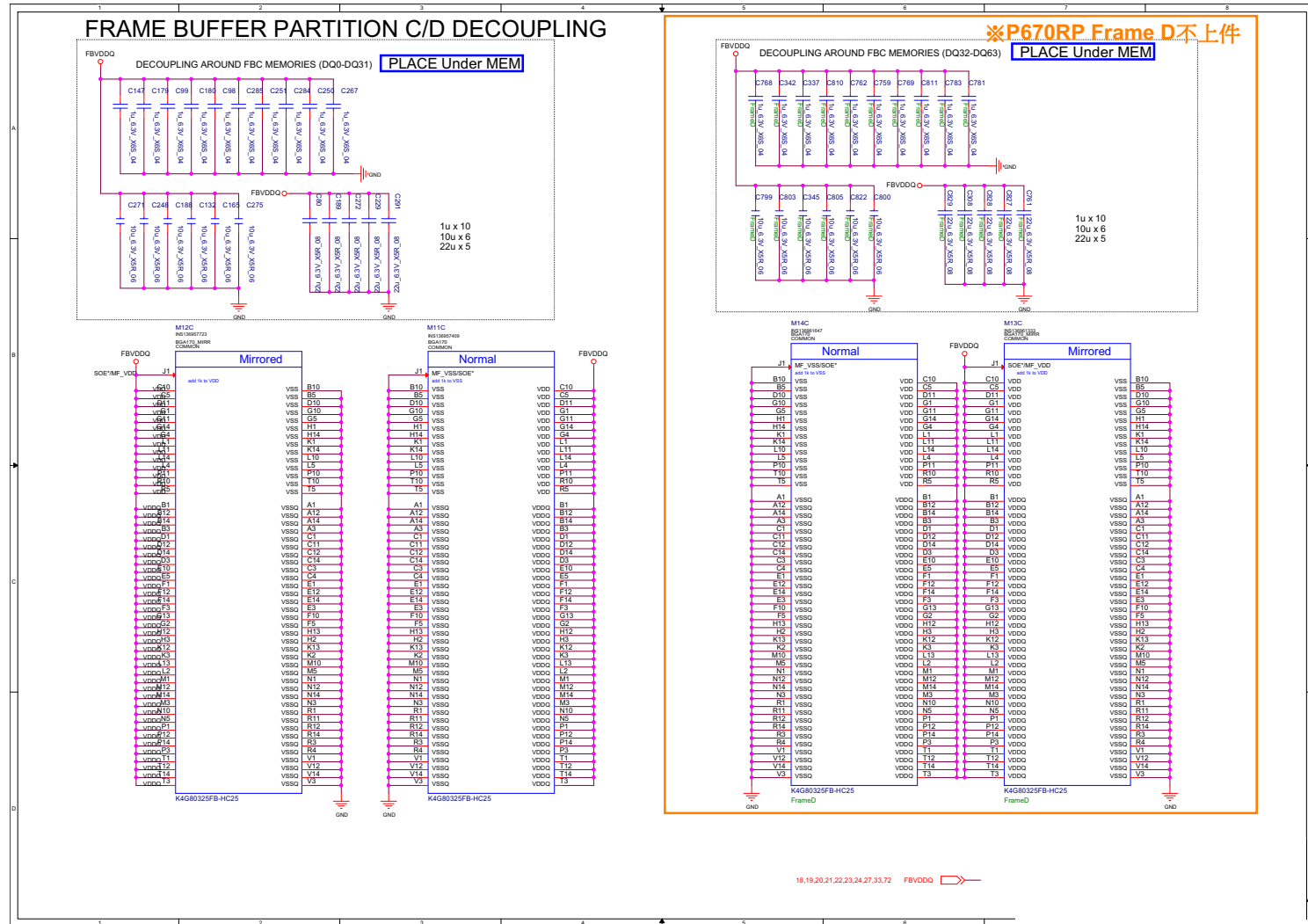


Sheet 23 of 91
Frame Buffer
Partition C



Frame Buffer Partition C_D

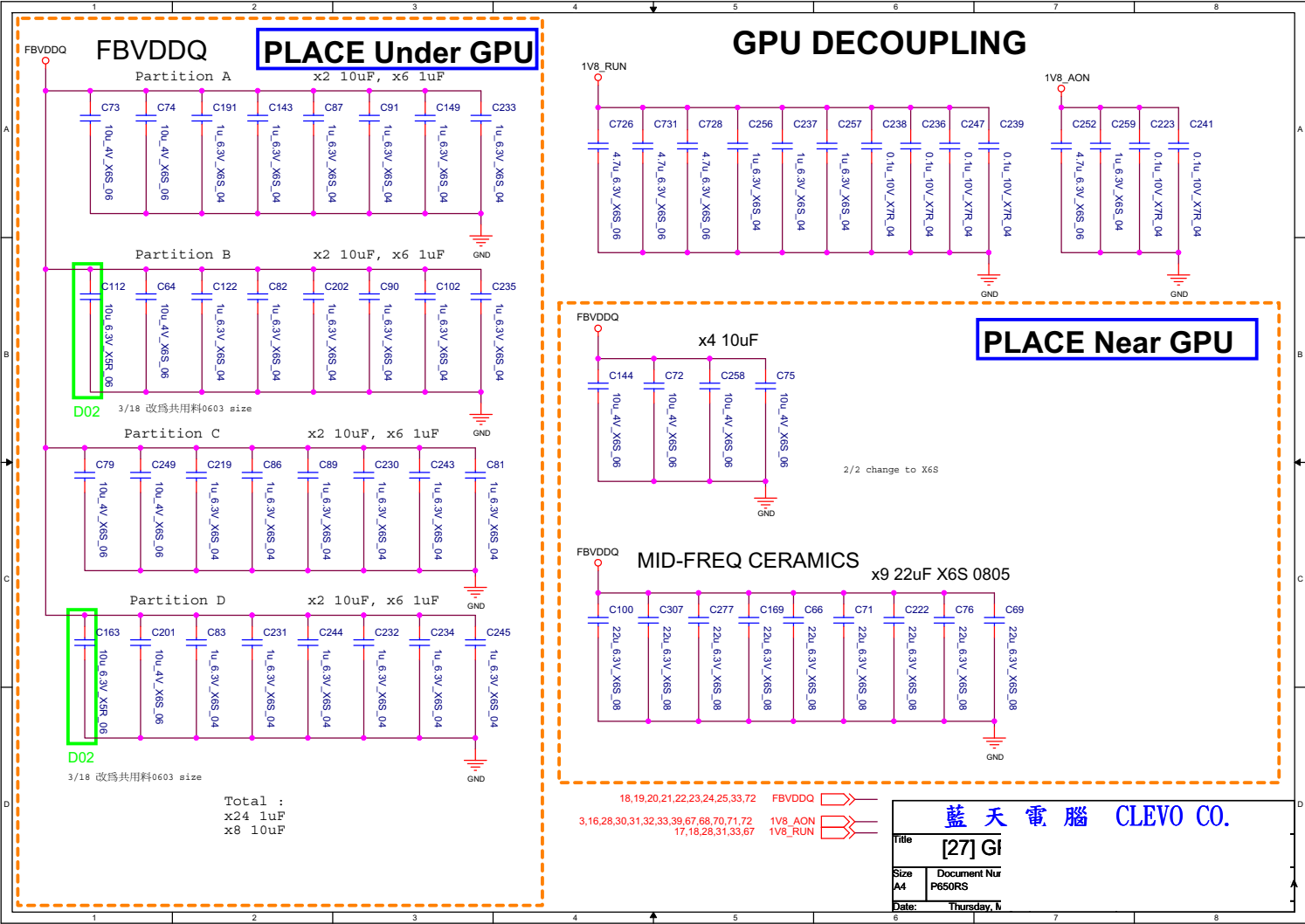
Sheet 25 of 91
Frame Buffer
Partition C_D



GPU Decoupling 1 B - 27



GPU Decoupling 2

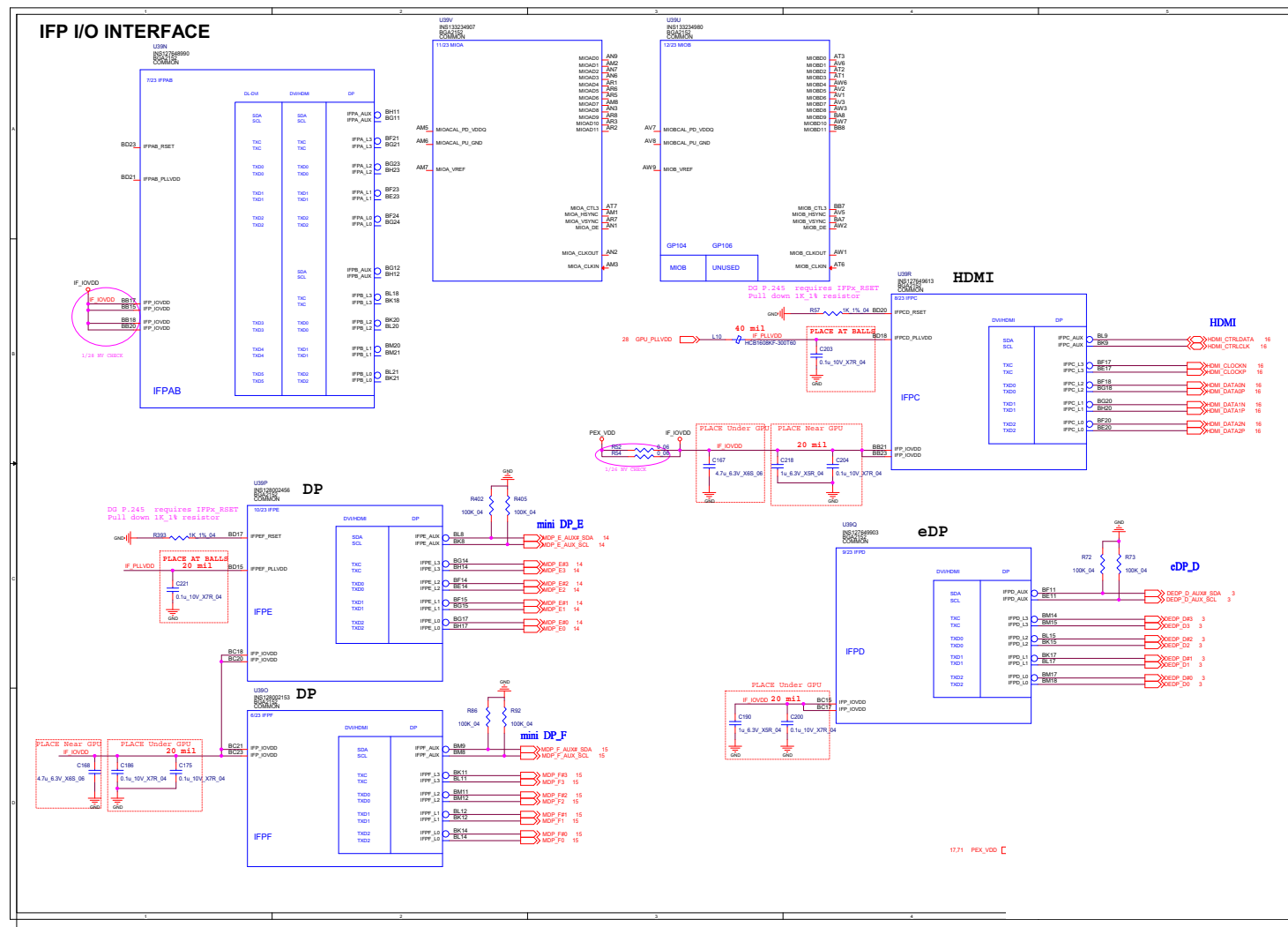


Straps & XTAL B - 29

IFP I/O Interface

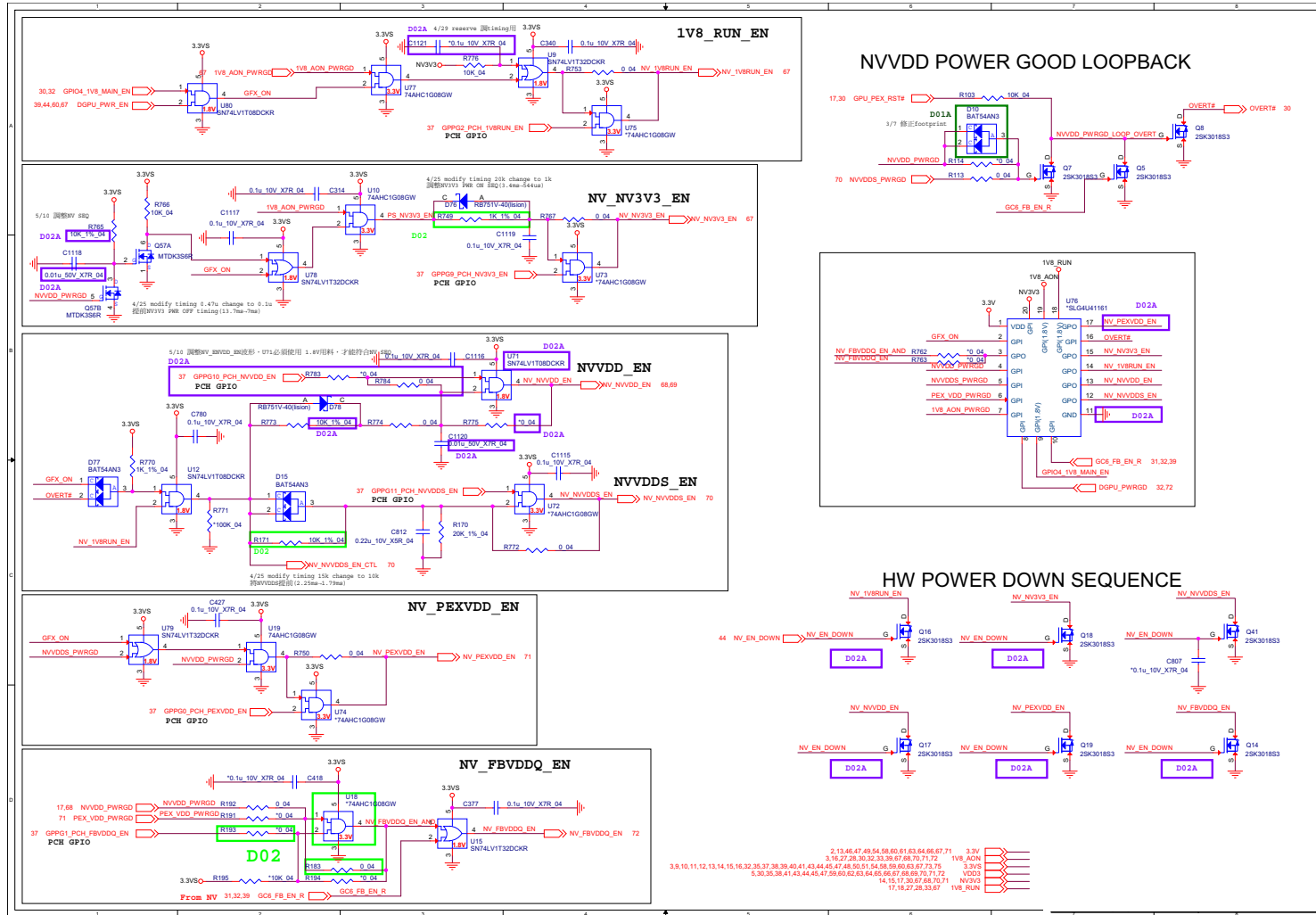
B. Schematic Diagrams

Sheet 29 of 91
IFP I/O Interface

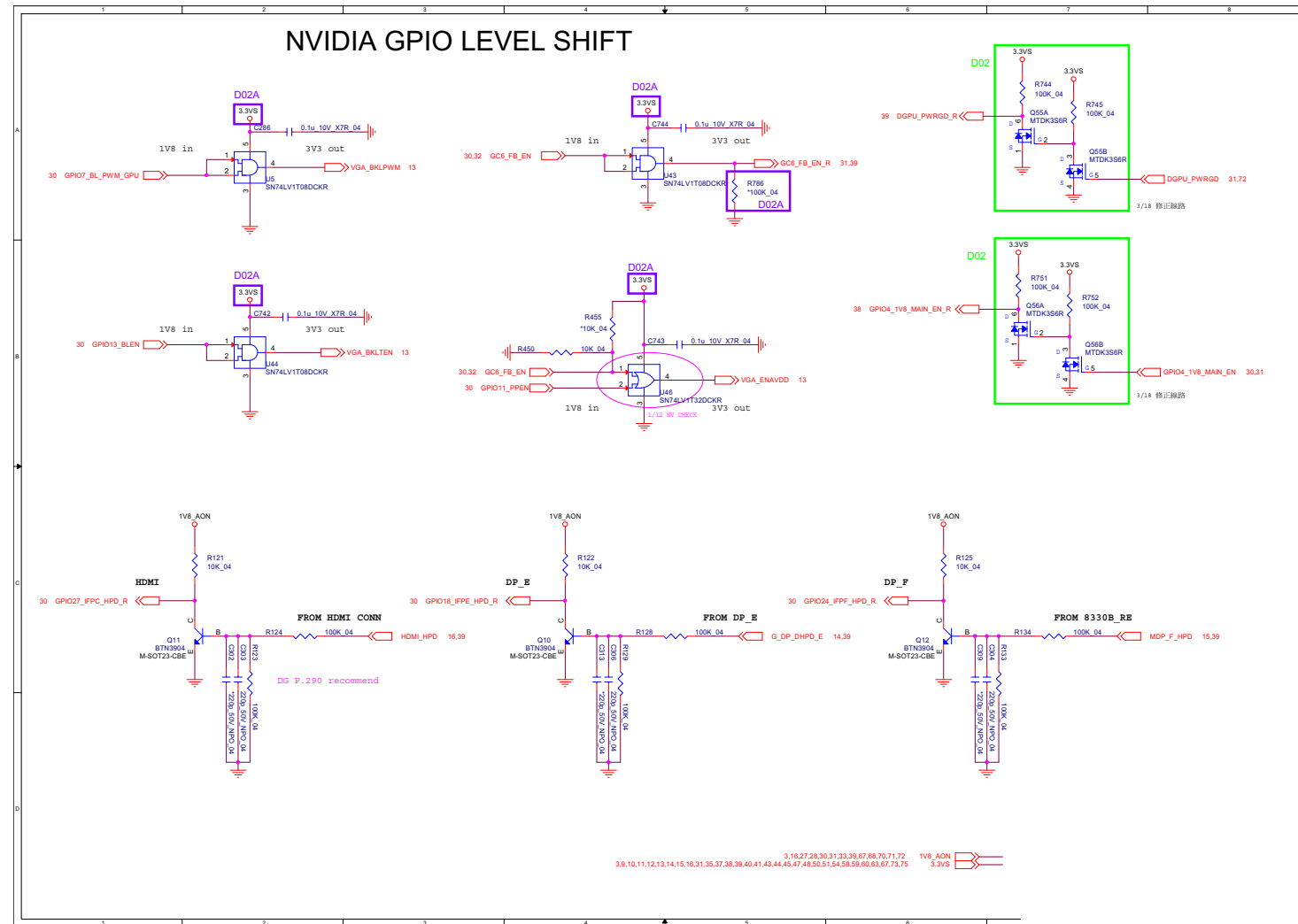


NVIDIA Power Sequence

Sheet 31 of 91
NVIDIA Power
Sequence

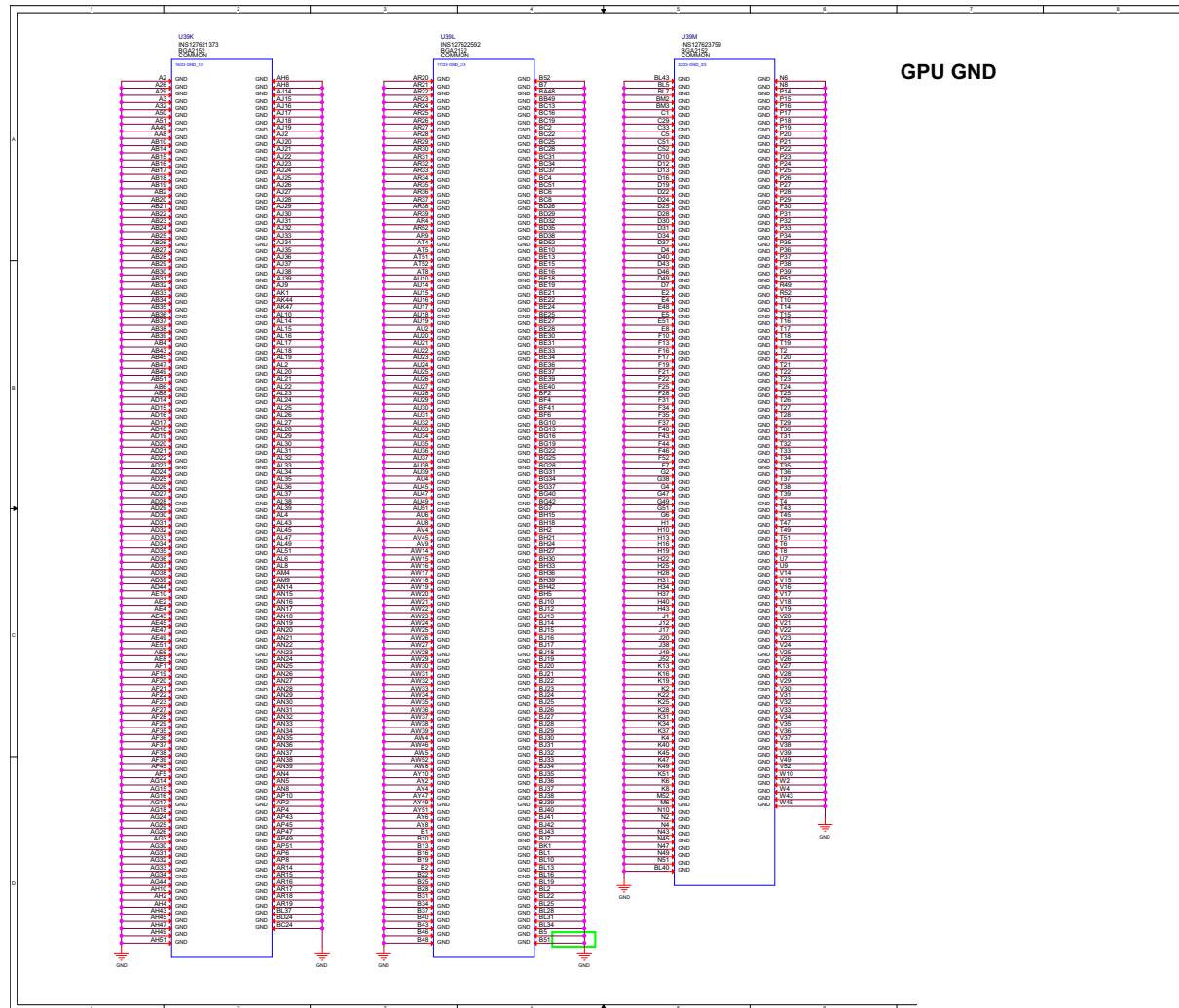


GPIO Level Shift



Sheet 32 of 91
GPIO Level Shift

GPU GND

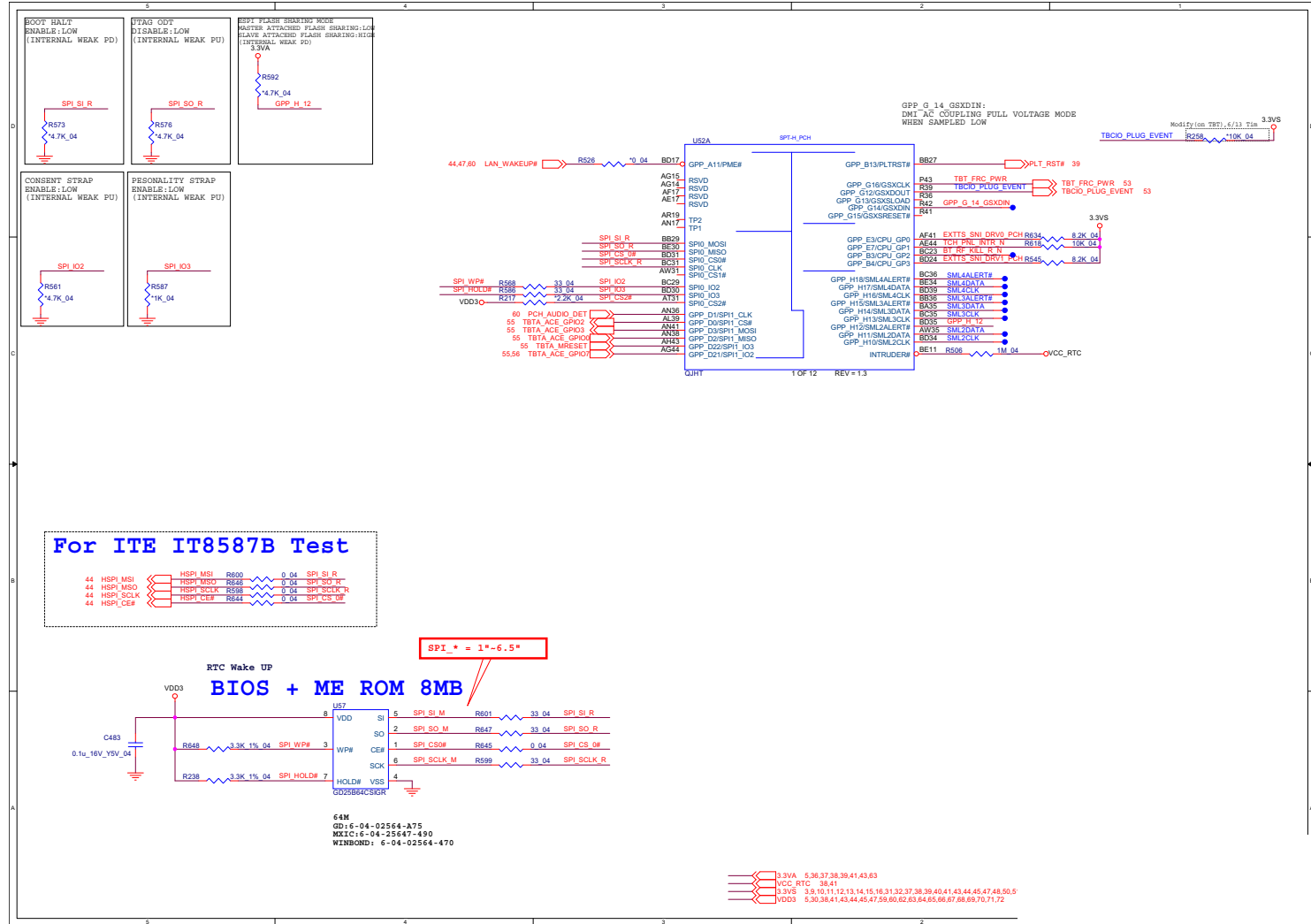


Sheet 34 of 91
GPU GND

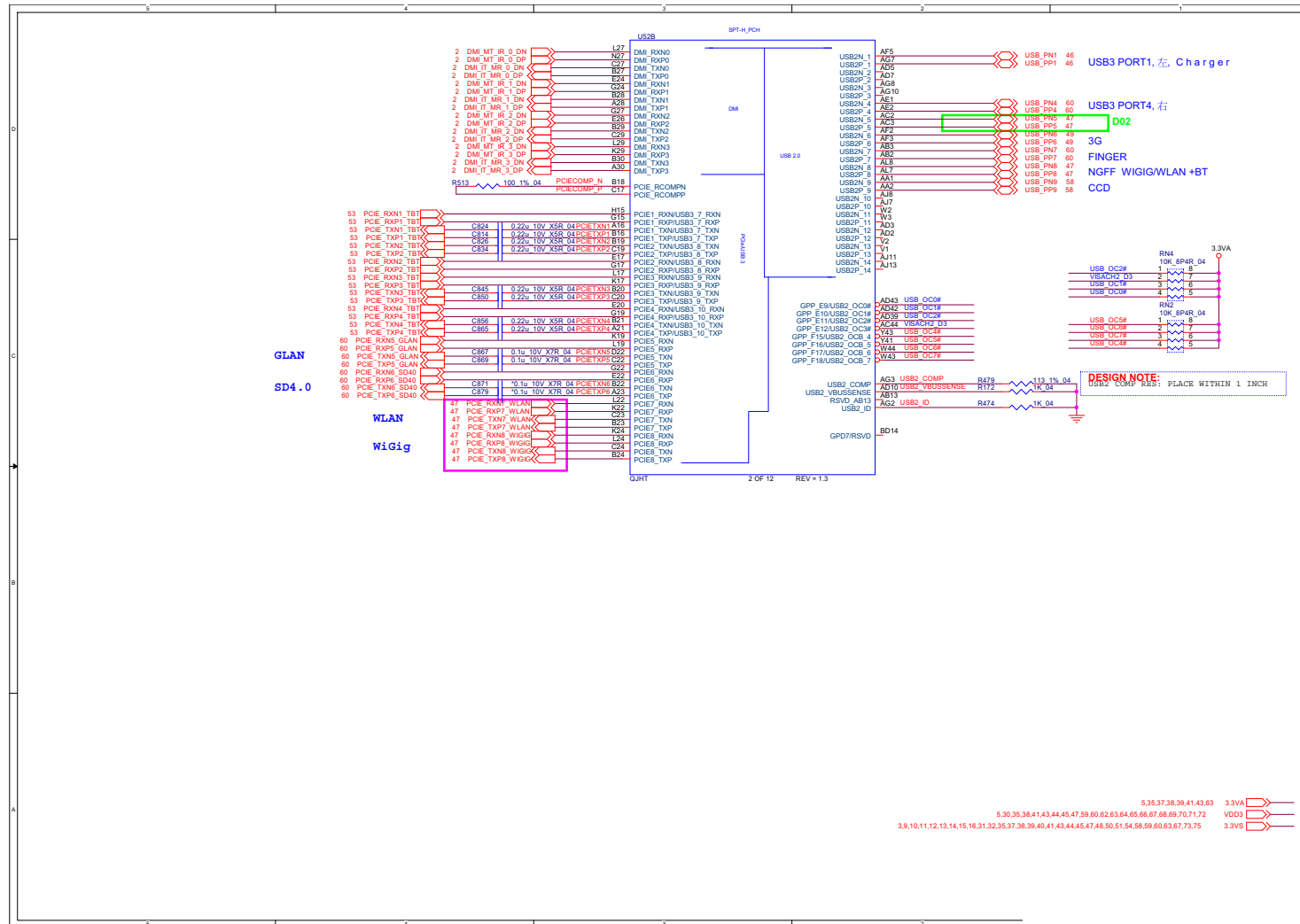
B.Schematic Diagrams

PCH 1/9

Sheet 35 of 91
PCH 1/9

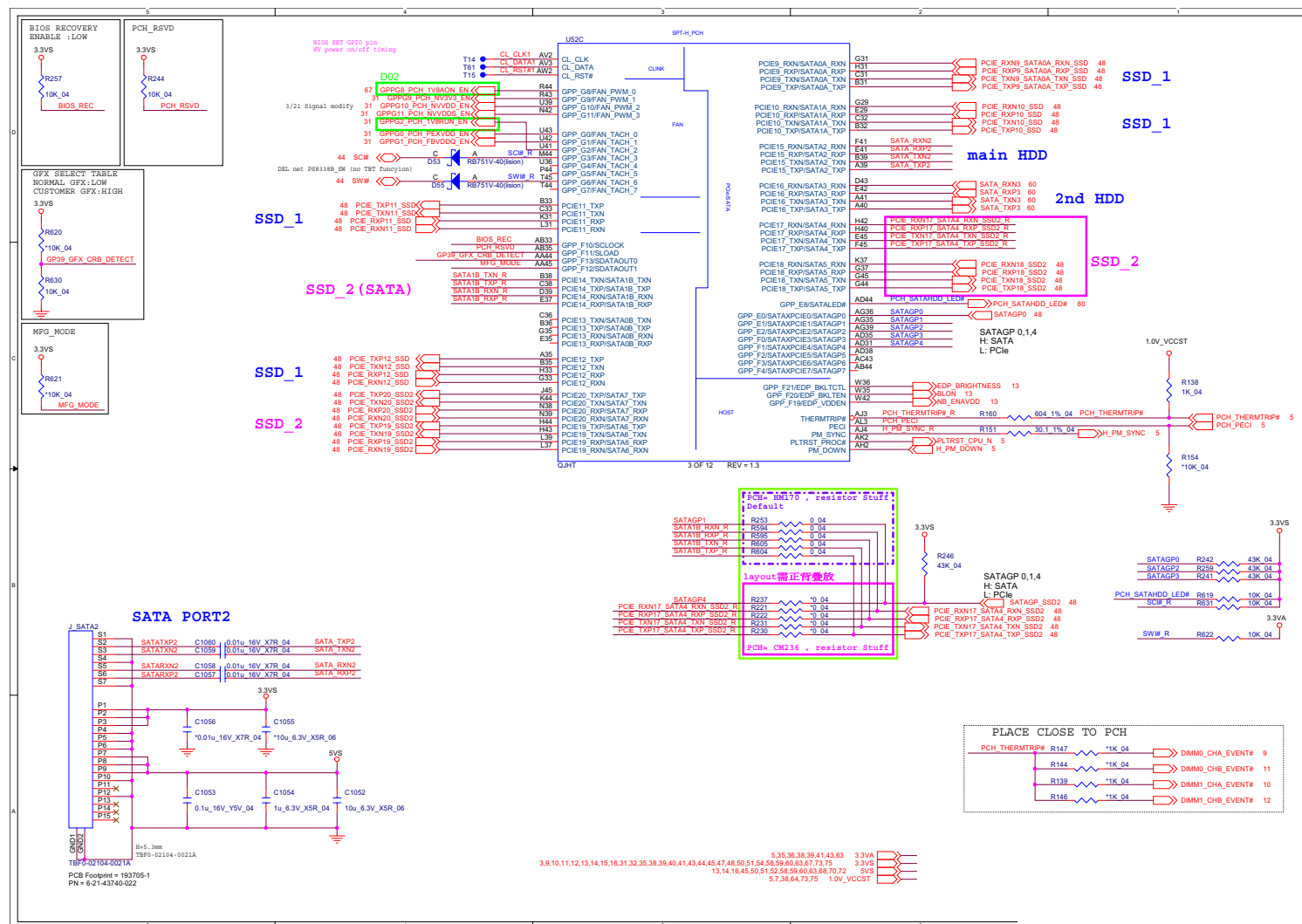


PCH 2/9

Sheet 36 of 91
PCH 2/9

PCH 3/9

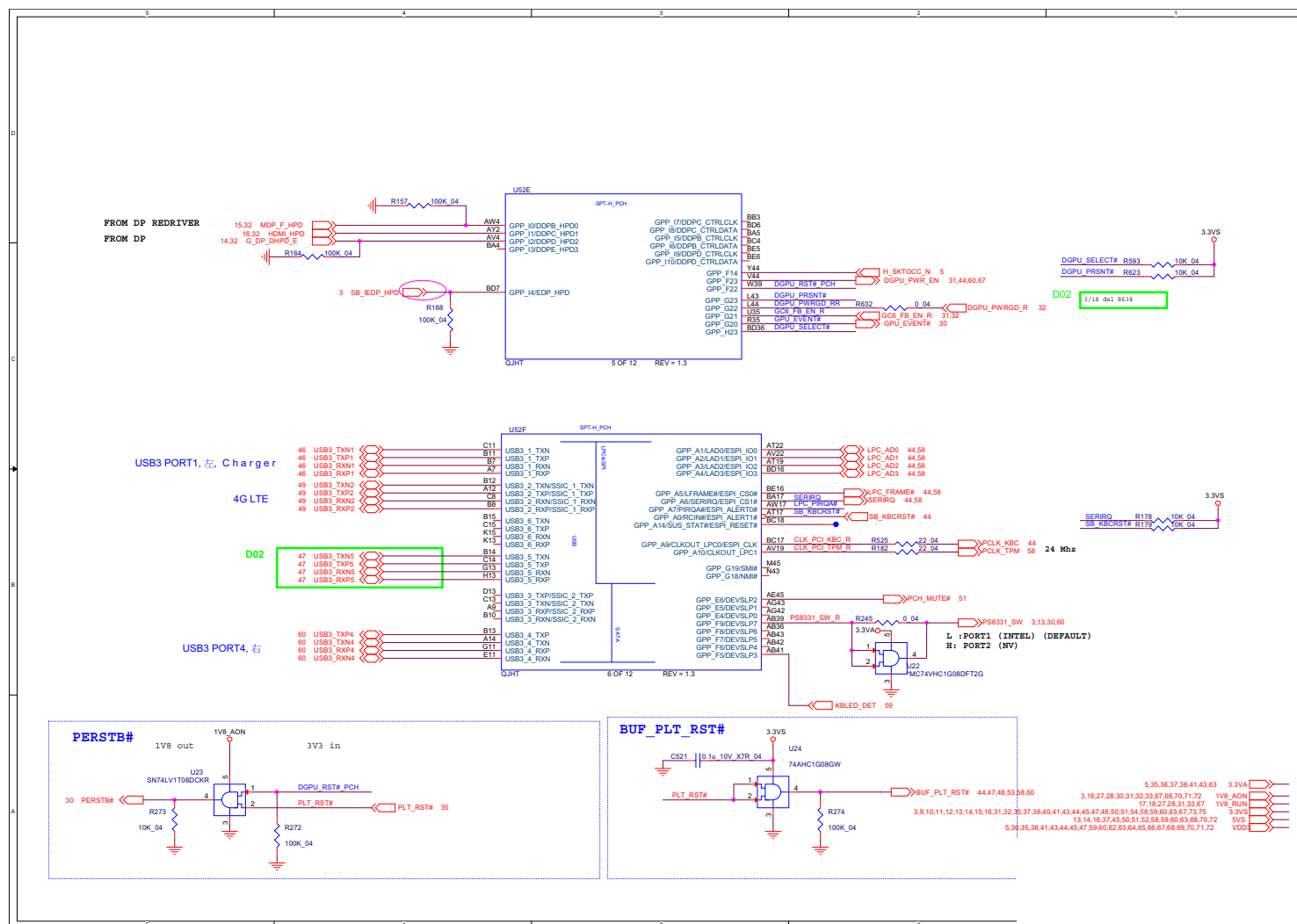
Sheet 37 of 91
PCH 3/9



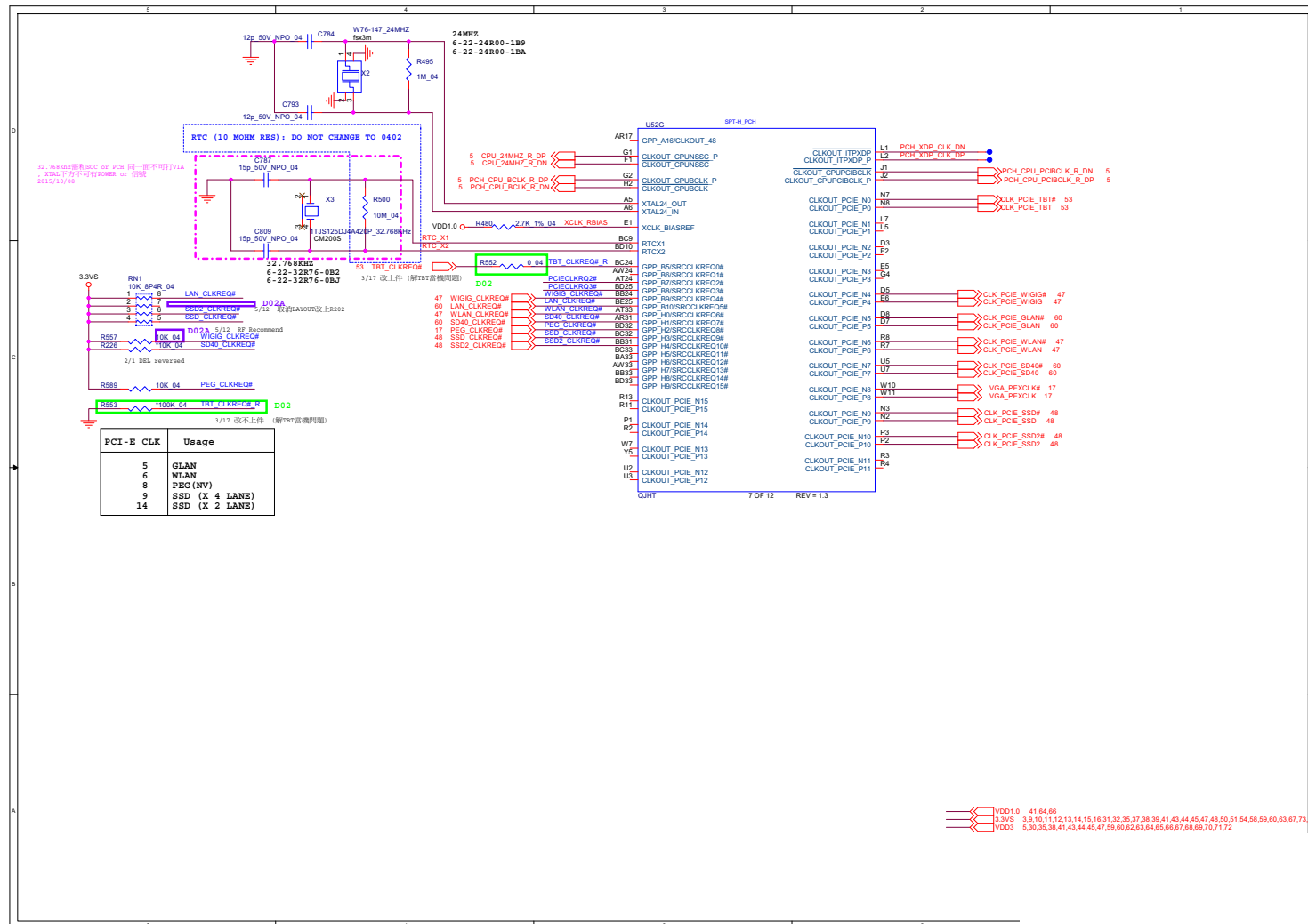
B.Schematic Diagrams

PCH 5/9

Sheet 39 of 91
PCH 5/9



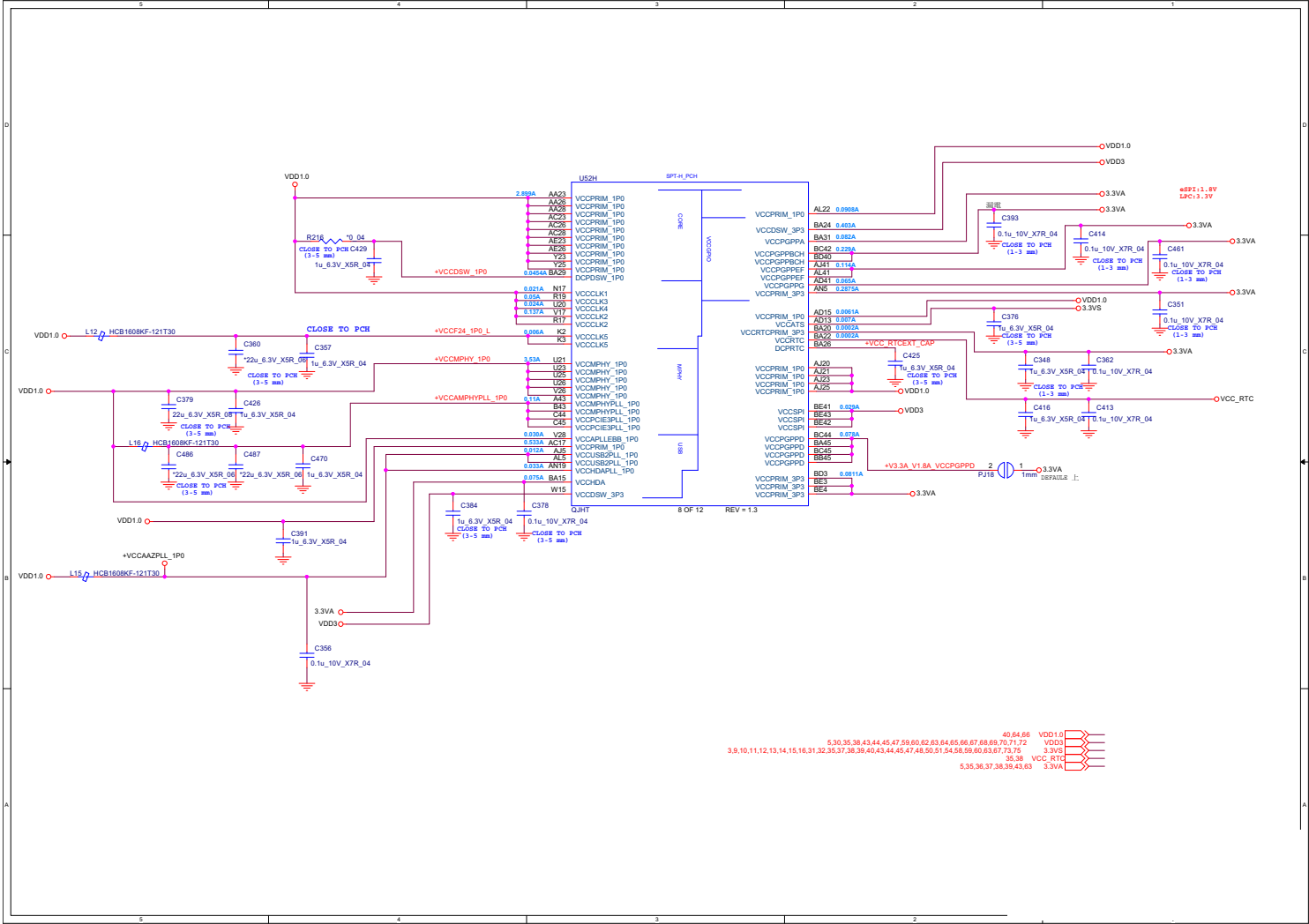
PCH 6/9 B - 41



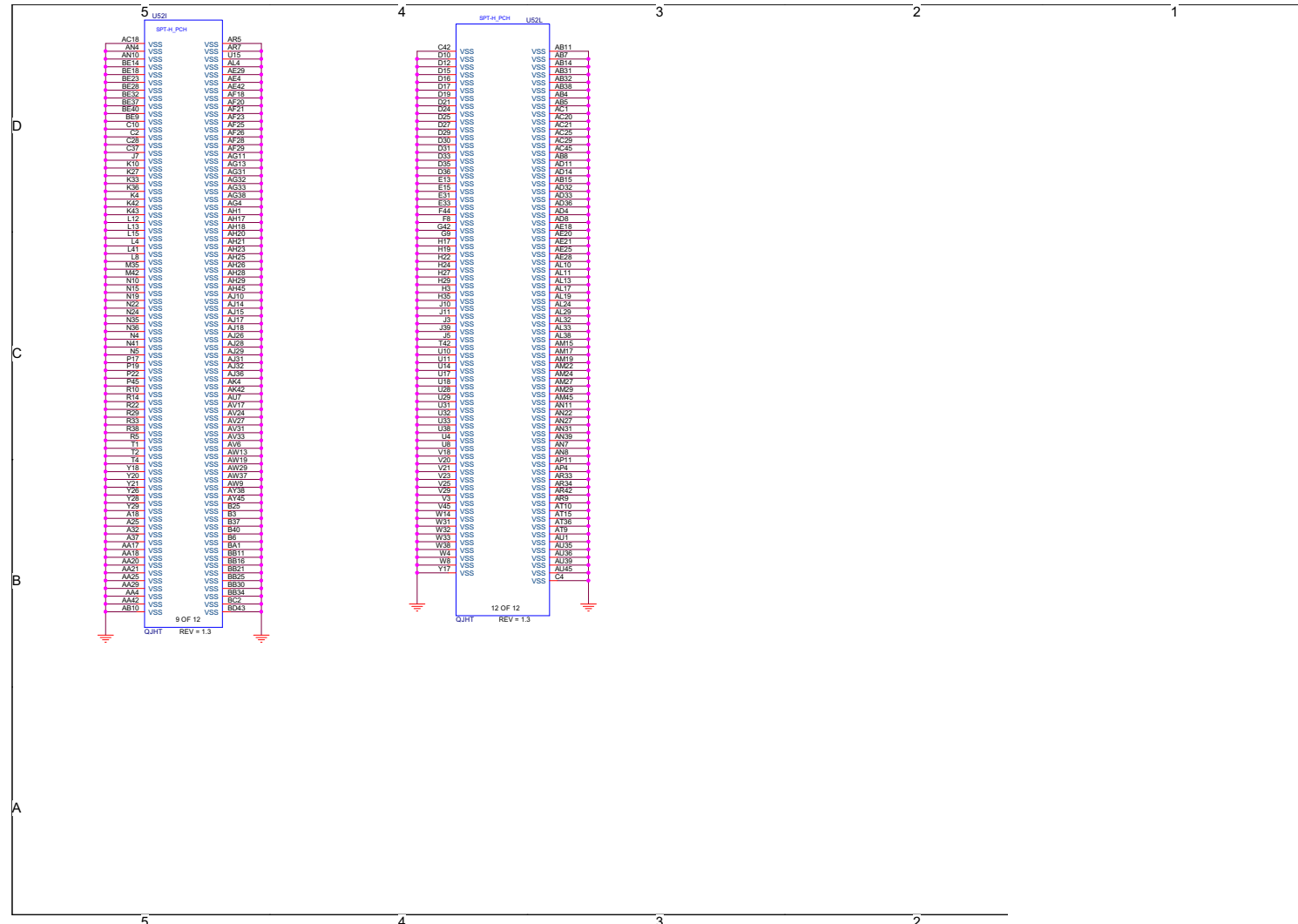
PCH 7/9

B.Schematic Diagrams

Sheet 41 of 91
PCH 7/9



PCH 8/9



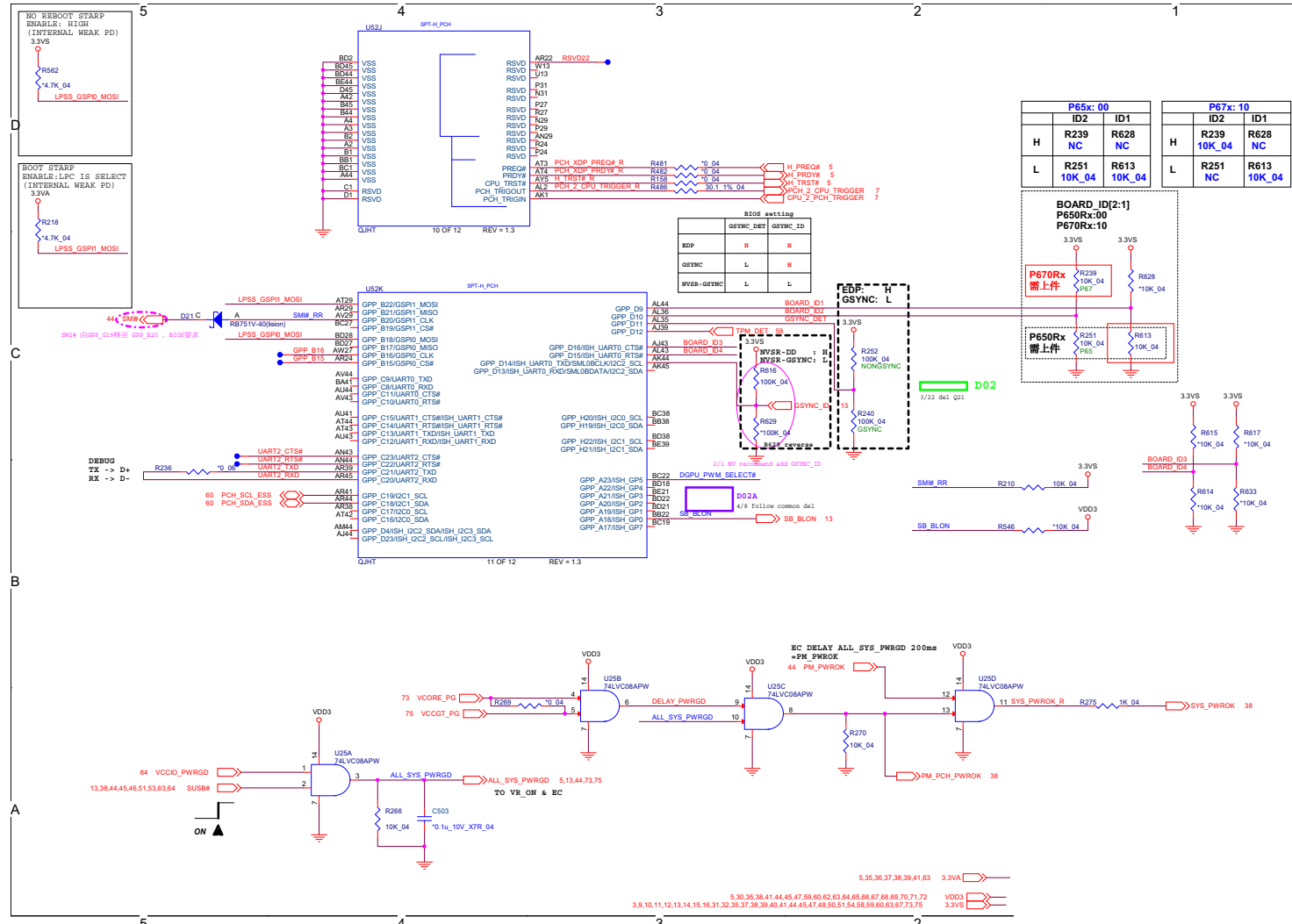
Sheet 42 of 91
PCH 8/9

B.Schematic Diagrams

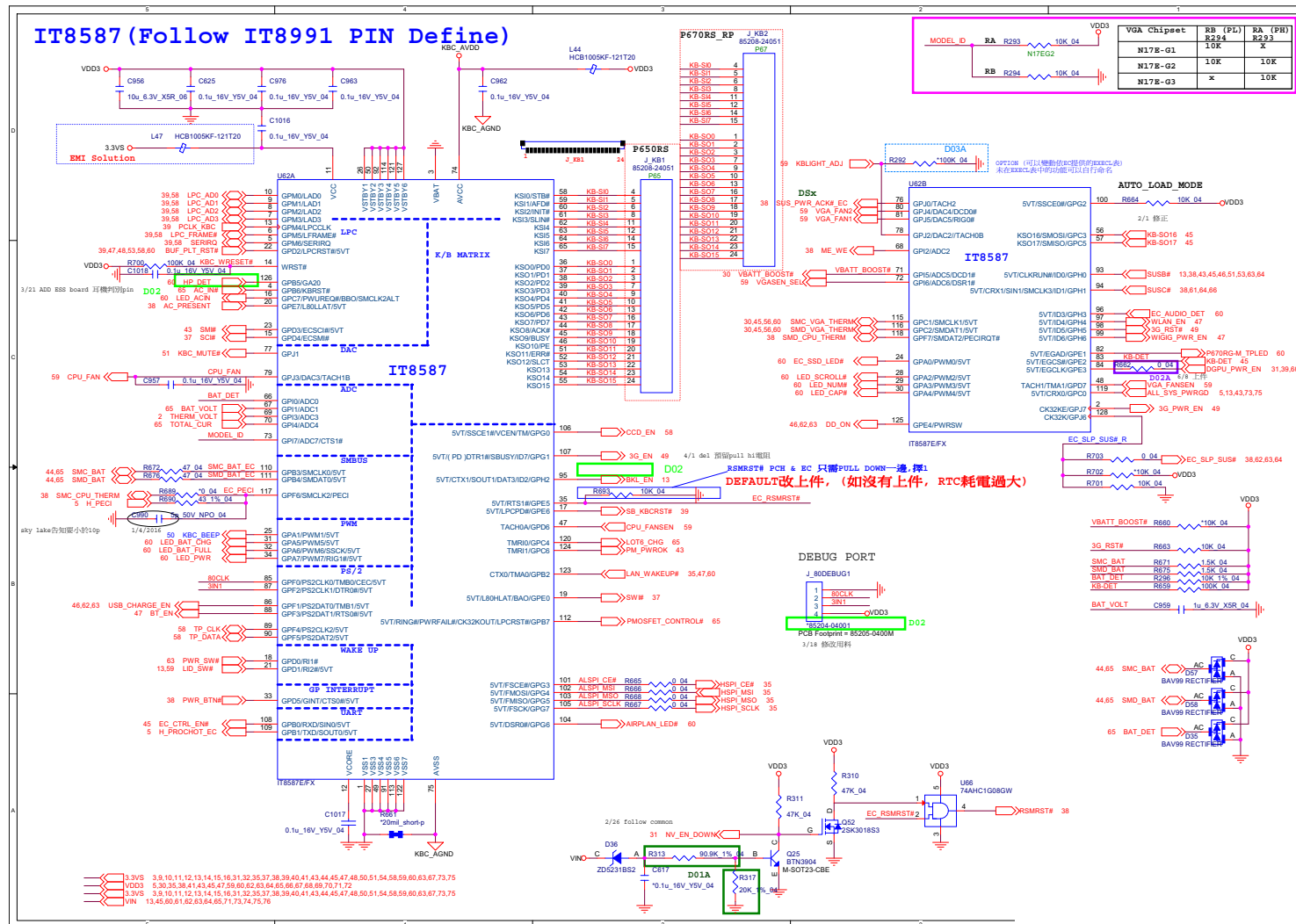
Schematic Diagrams

PCH 9/9

Sheet 43 of 91
PCH 9/9

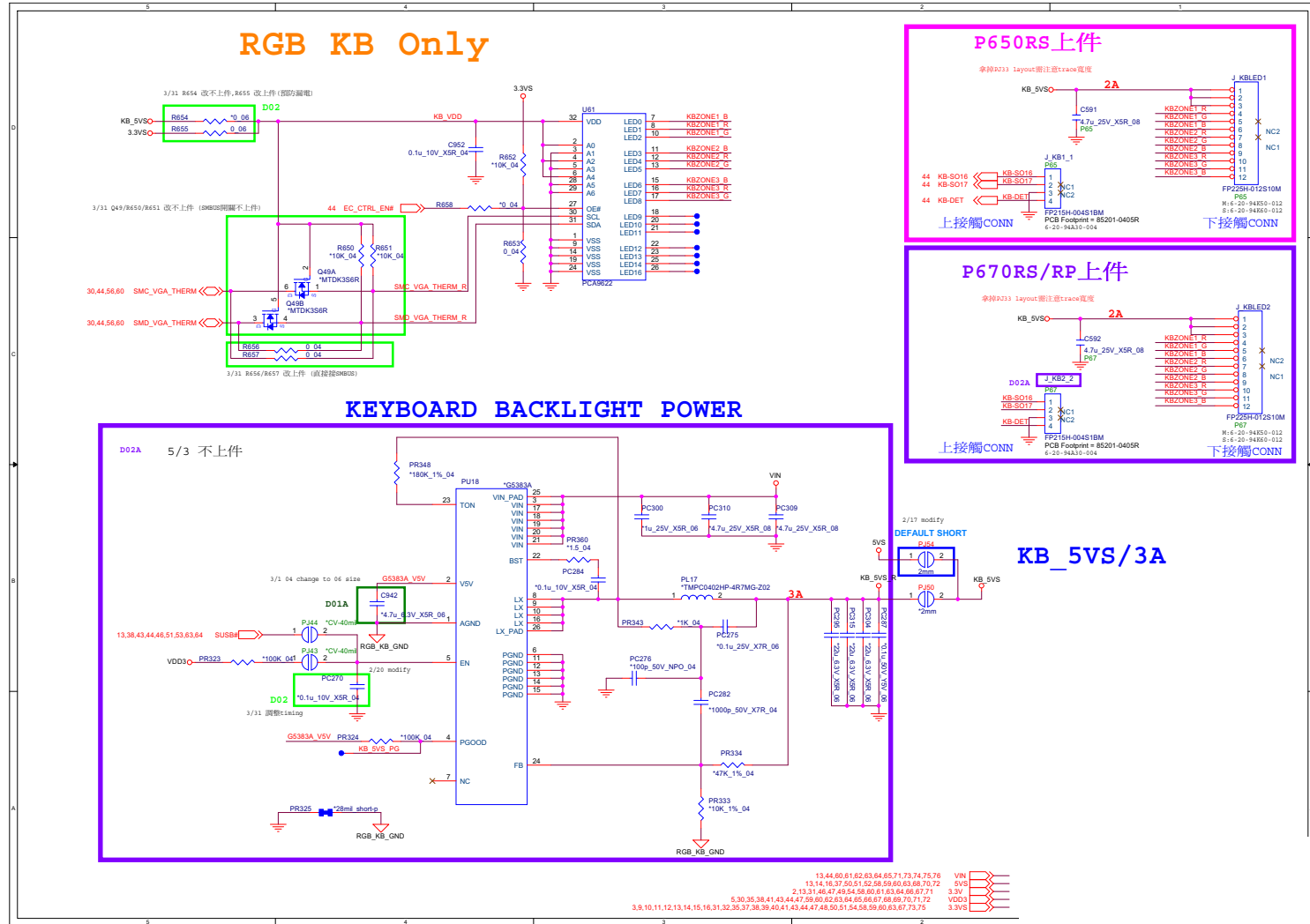


KBC IT8587

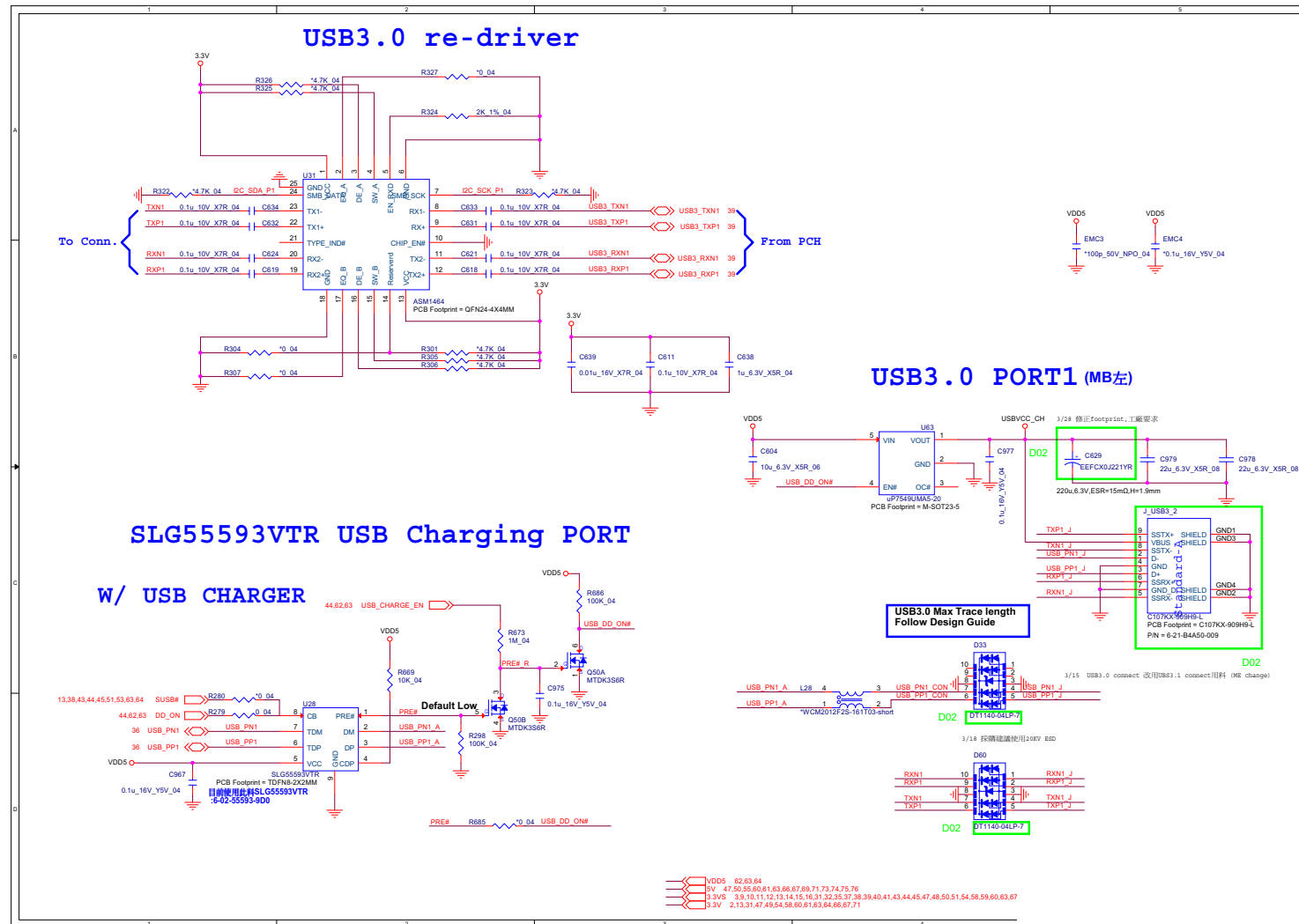


RGB KB Only

Sheet 45 of 91
RGB KB only



USB Charger

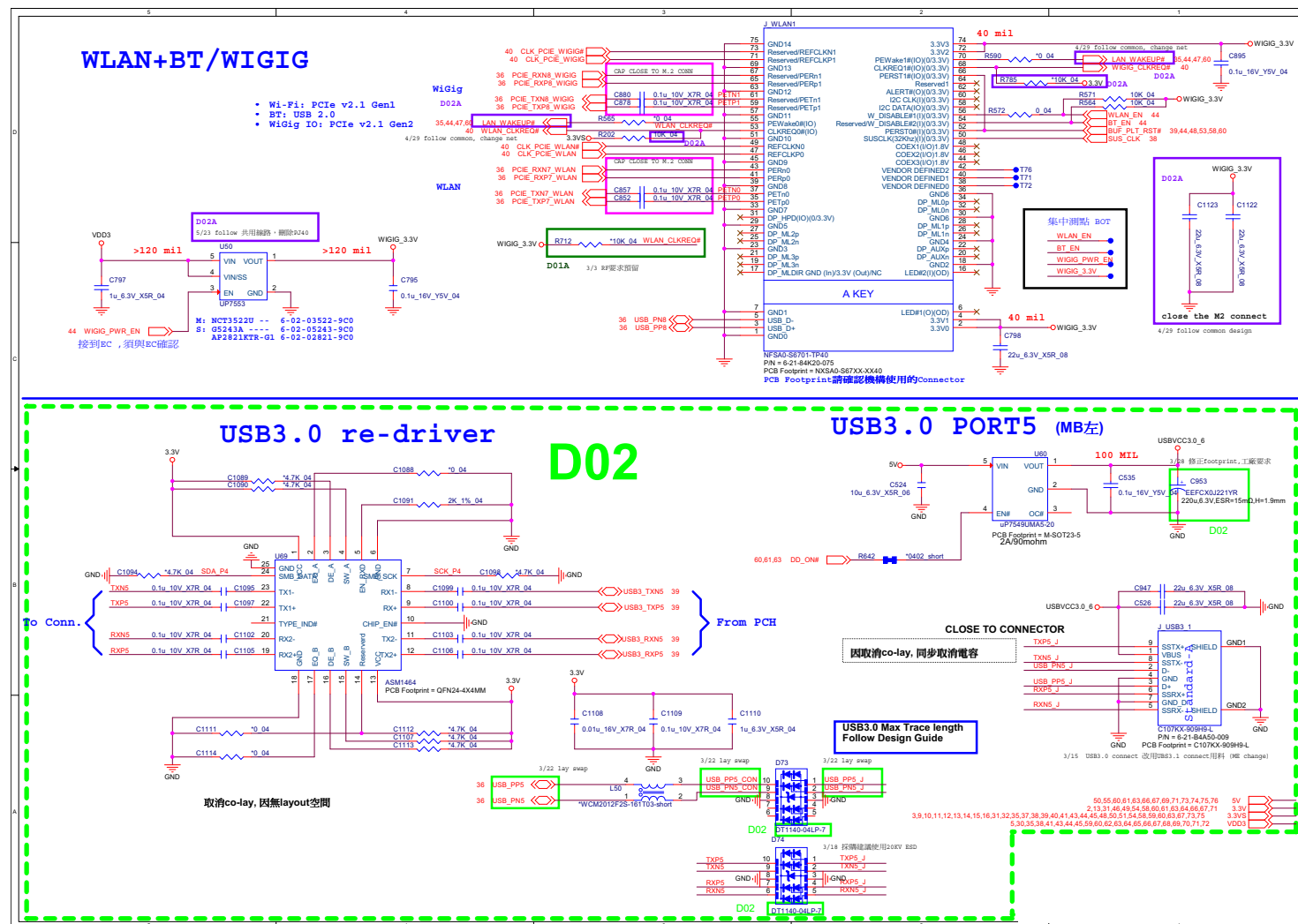


Sheet 46 of 91
USB Charger

M.2 WiGig/WLAN + BT

B. Schematic Diagrams

Sheet 47 of 91
M.2 WiGig/WLAN +
BT



NGFF_M (M2) SSD_1 (PCIe 4X)

SATAPG0
L: PCIe
H: SATA

C986 0.22u 10V XSR_04
C987 0.22u 10V XSR_04
C988 0.22u 10V XSR_04
C989 0.22u 10V XSR_04
C884 0.22u 10V XSR_04
C885 0.22u 10V XSR_04
C886 0.22u 10V XSR_04
C887 0.22u 10V XSR_04
C862 0.22u 10V XSR_04
C863 0.22u 10V XSR_04

PCE_TXP9_SATAA_TXP_SSD R
PCE_TXN9_SATAA_TXN_SSD R
PCE_RXN9_SATAA_RXN_SSD
PCE_RXP9_SATAA_RXP_SSD
PCE_TXP10_SSD
PCE_TXN10_SSD
PCE_RXN10_SSD
PCE_TXP11_SSD
PCE_TXN11_SSD
PCE_RXN11_SSD
PCE_TXP12_SSD
PCE_TXN12_SSD
PCE_RXN12_SSD

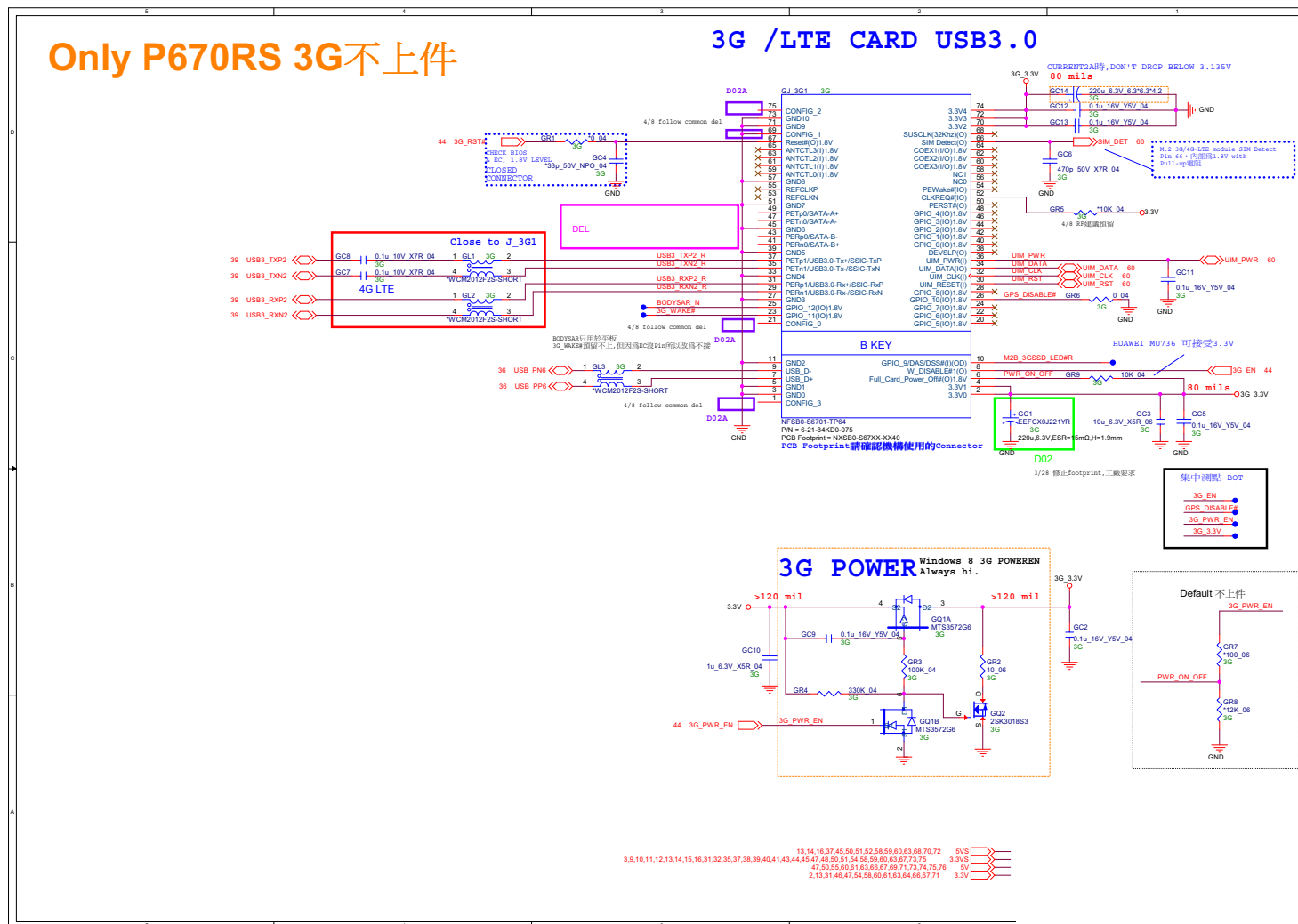
J_SSD1
M KEY
GND10
REFCLKP
REFCLKN
GND9
PETQ(SATA-A+)
PETQ(SATA-A-)
PERQ(SATA-B-)
PERQ(SATA-B+)
GND7
PETP1
NC9
NC8
NC7
PERP1
GND5
PETP2
PETQ2
GND4
PERQ2
PERP2
GND3
PETP3
3.3V3
GND2
PERP3
GND1
GND0

NFSM-S8701-TP64
PIN = 6-21-84KE0-075
PCB Footprint = NXSMO-S87XX-XX40
PCB Footprint=請確認機構使用的Connector

>120 mil
C1029 0.1u 10V_XTR_04
C1034 0.1u 10V_XTR_04
C1036 10u_6.3V_XSR_06
C1033 0.1u 10V_XTR_04
C1032 0.1u 10V_XTR_04
C1036 10u_6.3V_XSR_06
C1031 0.1u 10V_XTR_04
C1030 0.1u 10V_XTR_04
C1021 0.1u 10V_XTR_04
C1022 0.1u 10V_XTR_04
C1023 0.1u 10V_XTR_04
C1024 0.1u 10V_XTR_04
C1025 0.1u 10V_XTR_04
C1026 0.1u 10V_XTR_04
C1027 0.1u 10V_XTR_04
C1028 0.1u 10V_XTR_04
C1029 0.1u 10V_XTR_04
C1030 0.1u 10V_XTR_04
C1031 0.1u 10V_XTR_04
C1032 0.1u 10V_XTR_04
C1033 0.1u 10V_XTR_04
C1034 0.1u 10V_XTR_04
C1035 0.1u 10V_XTR_04
C1036 0.1u 10V_XTR_04
C1037 0.1u 10V_XTR_04
C1038 0.1u 10V_XTR_04
C1039 0.1u 10V_XTR_04
C1040 0.1u 10V_XTR_04
C1041 0.1u 10V_XTR_04
C1042 0.1u 10V_XTR_04
C1043 0.1u 10V_XTR_04
C1044 0.1u 10V_XTR_04
C1045 0.1u 10V_XTR_04
C1046 0.1u 10V_XTR_04
C1047 0.1u 10V_XTR_04
C1048 0.1u 10V_XTR_04
C1049 0.1u 10V_XTR_04
C1050 0.1u 10V_XTR_04
C1051 0.1u 10V_XTR_04
C1052 0.1u 10V_XTR_04
C1053 0.1u 10V_XTR_04
C1054 0.1u 10V_XTR_04
C1055 0.1u 10V_XTR_04
C1056 0.1u 10V_XTR_04
C1057 0.1u 10V_XTR_04
C1058 0.1u 10V_XTR_04
C1059 0.1u 10V_XTR_04
C1060 0.1u 10V_XTR_04
C1061 0.1u 10V_XTR_04
C1062 0.1u 10V_XTR_04
C1063 0.1u 10V_XTR_04
C1064 0.1u 10V_XTR_04
C1065 0.1u 10V_XTR_04
C1066 0.1u 10V_XTR_04
C1067 0.1u 10V_XTR_04
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C1069 0.1u 10V_XTR_04
C1070 0.1u 10V_XTR_04
C1071 0.1u 10V_XTR_04
C1072 0.1u 10V_XTR_04
C1073 0.1u 10V_XTR_04
C1074 0.1u 10V_XTR_04
C1075 0.1u 10V_XTR_04
C1076 0.1u 10V_XTR_04
C1077 0.1u 10V_XTR_04
C1078 0.1u 10V_XTR_04
C1079 0.1u 10V_XTR_04
C1080 0.1u 10V_XTR_04
C1081 0.1u 10V_XTR_04
C1082 0.1u 10V_XTR_04
C1083 0.1u 10V_XTR_04
C1084 0.1u 10V_XTR_04
C1085 0.1u 10V_XTR_04
C1086 0.1u 10V_XTR_04
C1087 0.1u 10V_XTR_04
C1088 0.1u 10V_XTR_04
C1089 0.1u 10V_XTR_04
C1090 0.1u 10V_XTR_04
C1091 0.1u 10V_XTR_04
C1092 0.1u 10V_XTR_04
C1093 0.1u 10V_XTR_04
C1094 0.1u 10V_XTR_04
C1095 0.1u 10V_XTR_04
C1096 0.1u 10V_XTR_04
C1097 0.1u 10V_XTR_04
C1098 0.1u 10V_XTR_04
C1099 0.1u 10V_XTR_04
C1100 0.1u 10V_XTR_04
C1101 0.1u 10V_XTR_04
C1102 0.1u 10V_XTR_04
C1103 0.1u 10V_XTR_04
C1104 0.1u 10V_XTR_04
C1105 0.1u 10V_XTR_04
C1106 0.1u 10V_XTR_04
C1107 0.1u 10V_XTR_04
C1108 0.1u 10V_XTR_04
C1109 0.1u 10V_XTR_04
C1110 0.1u 10V_XTR_04
C1111 0.1u 10V_XTR_04
C1112 0.1u 10V_XTR_04
C1113 0.1u 10V_XTR_04
C1114 0.1u 10V_XTR_04
C1115 0.1u 10V_XTR_04
C1116 0.1u 10V_XTR_04
C1117 0.1u 10V_XTR_04
C1118 0.1u 10V_XTR_04
C1119 0.1u 10V_XTR_04
C1120 0.1u 10V_XTR_04
C1121 0.1u 10V_XTR_04
C1122 0.1u 10V_XTR_04
C1123 0.1u 10V_XTR_04
C1124 0.1u 10V_XTR_04
C1125 0.1u 10V_XTR_04
C1126 0.1u 10V_XTR_04
C1127 0.1u 10V_XTR_04
C1128 0.1u 10V_XTR_04
C1129 0.1u 10V_XTR_04
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C1134 0.1u 10V_XTR_04
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C1166 0.1u 10V_XTR_04
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C1171 0.1u 10V_XTR_04

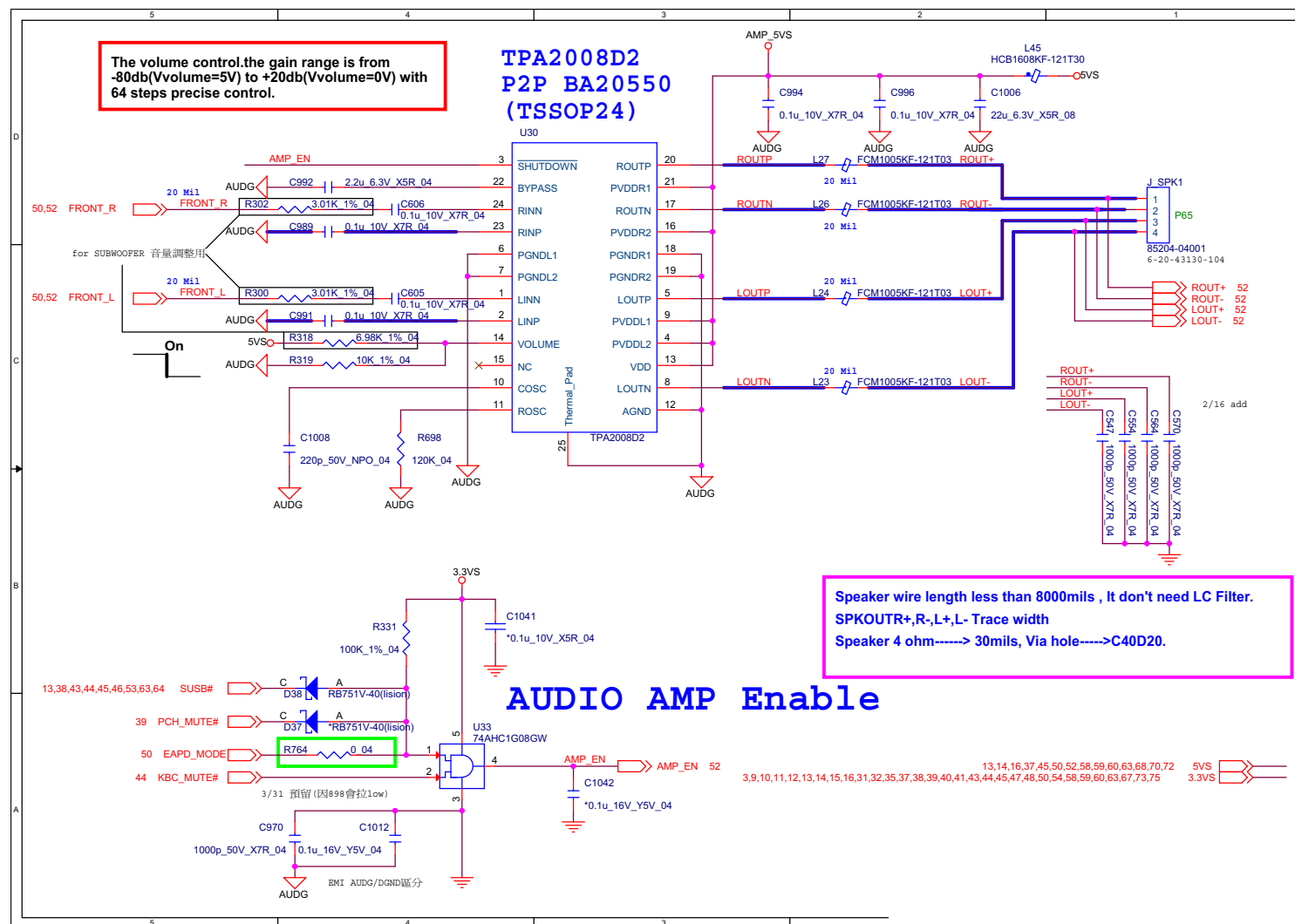
M.2 3G/LTE

Only P670RS 3G不上件

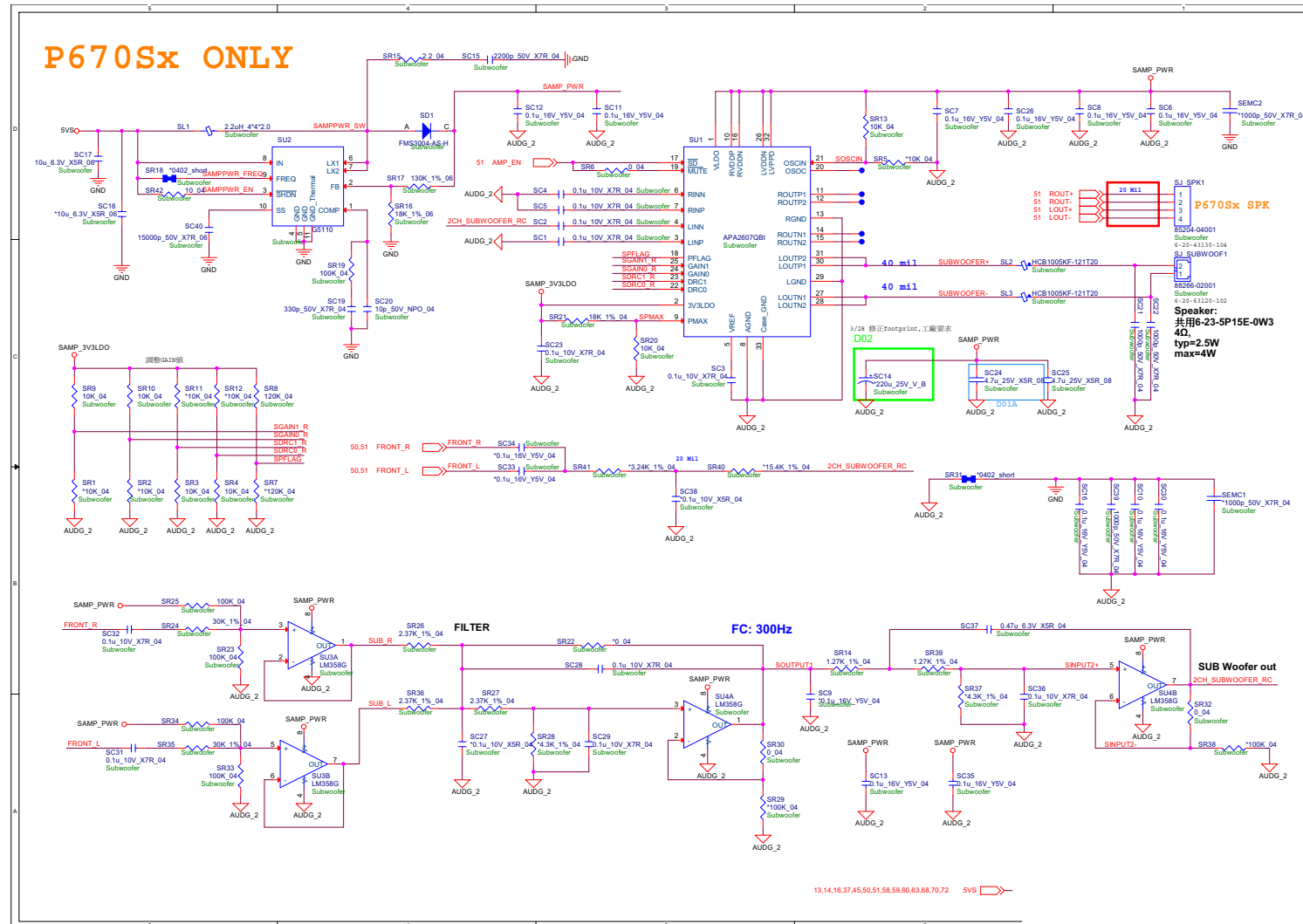


[illegible]

TPA2008D2

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TPA2008D2

Subwoofer



AR_TBT

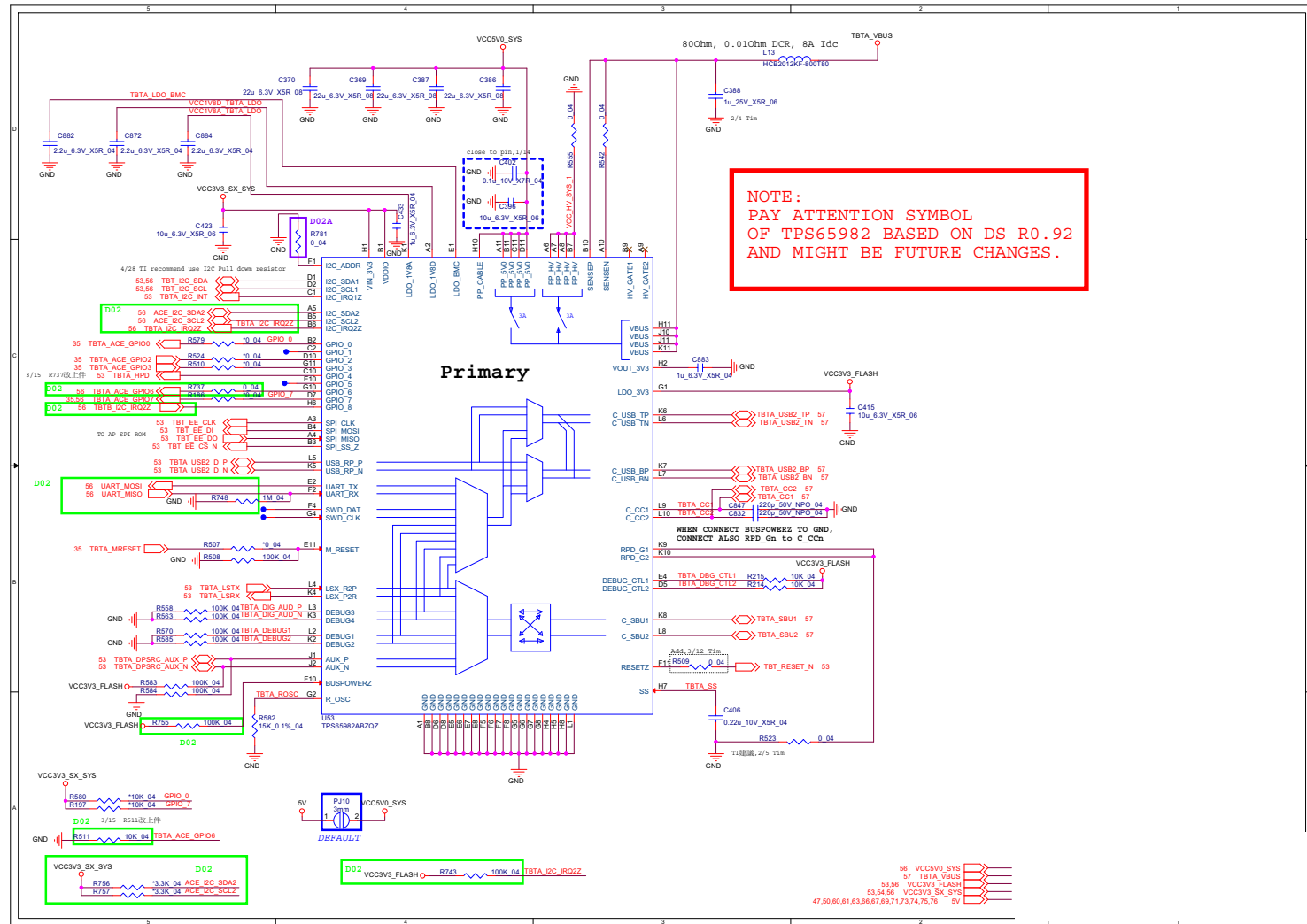
B.Schematic Diagrams

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Schematic Diagrams

TPS65982

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TPS65982

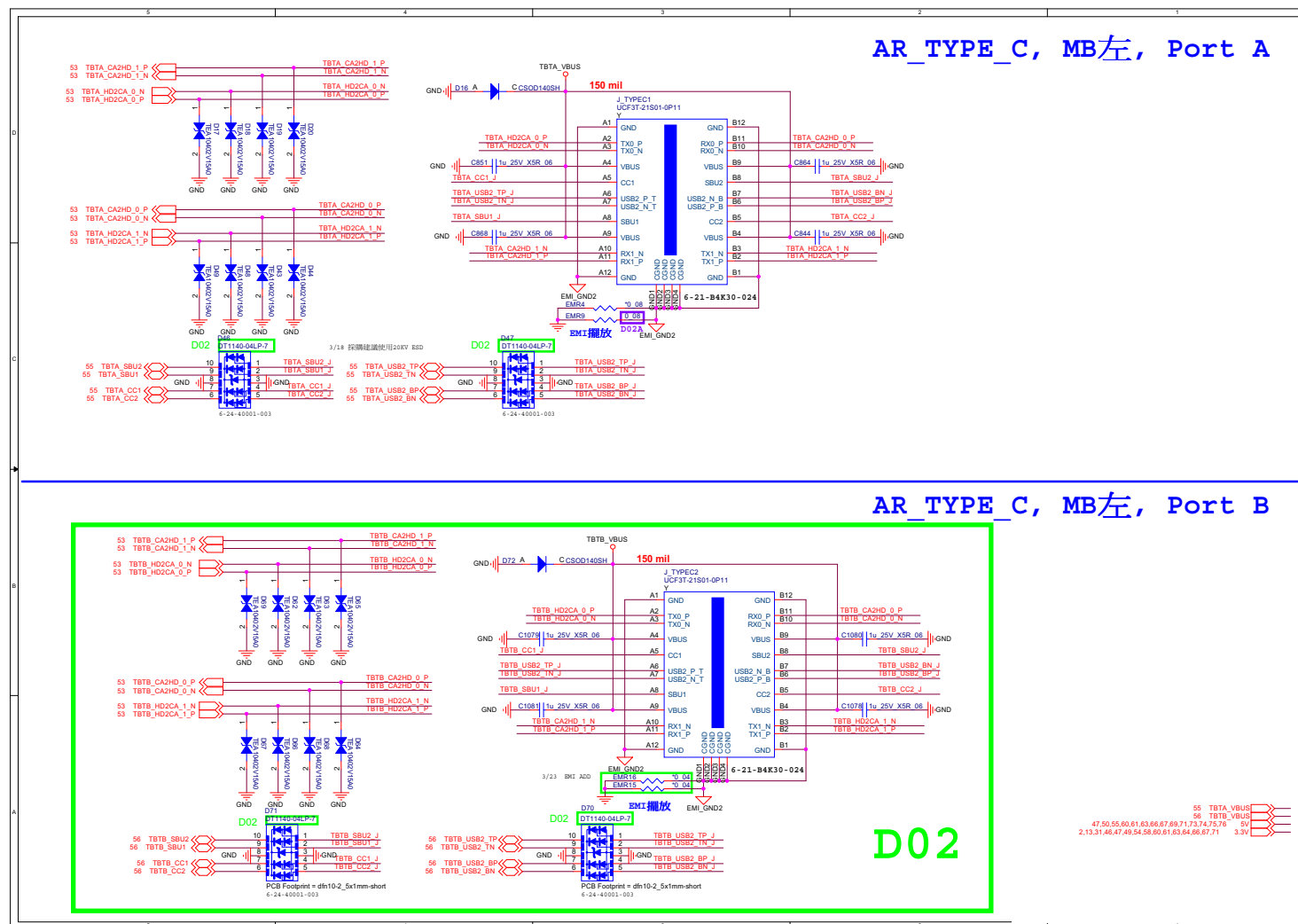


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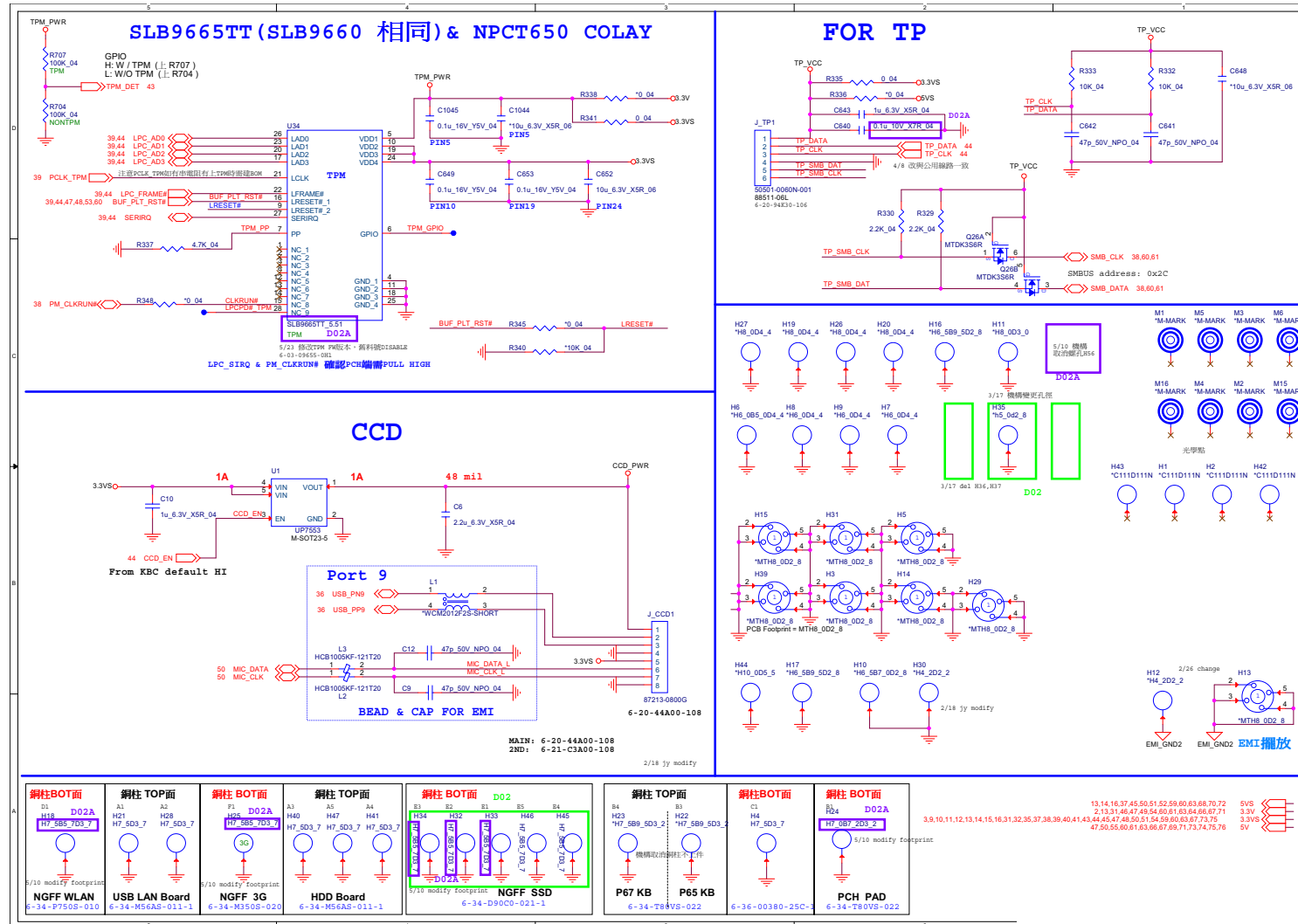
TPS65982-1 B - 57

AR_Conn

Sheet 57 of 91
AR_Conn

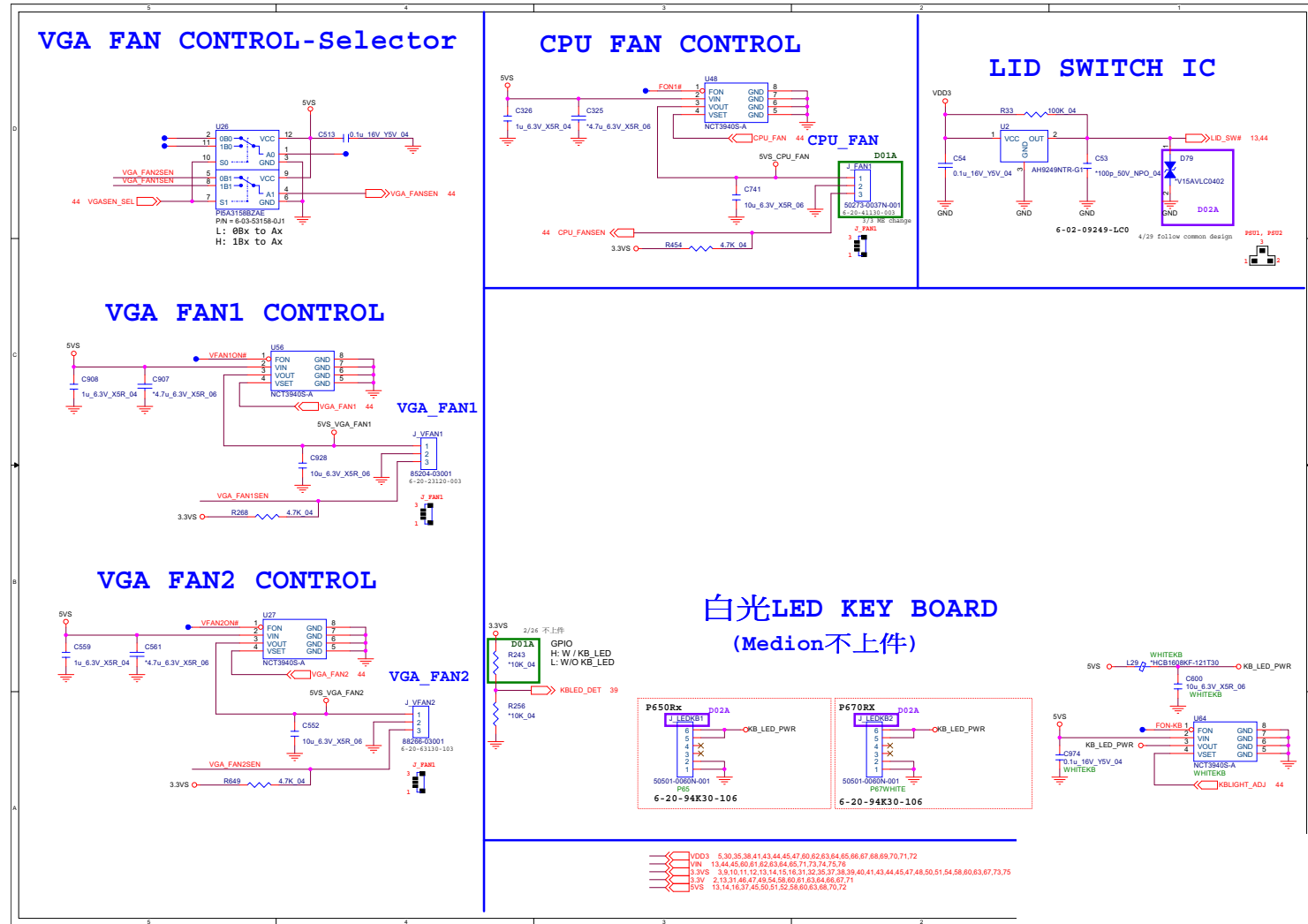


TPM, CCD, TP B - 59

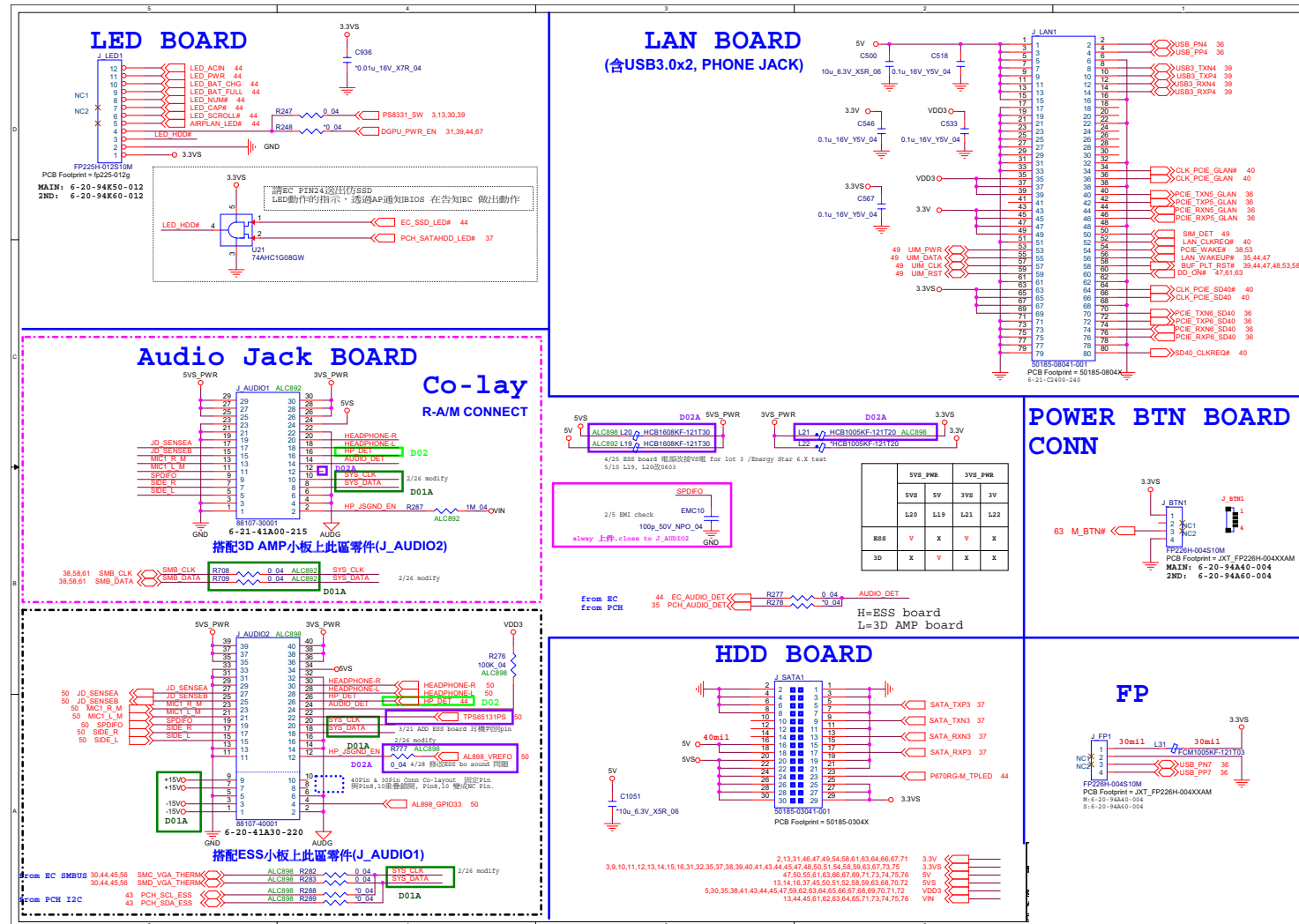


Schematic Diagrams

Fan, LID, KB LED

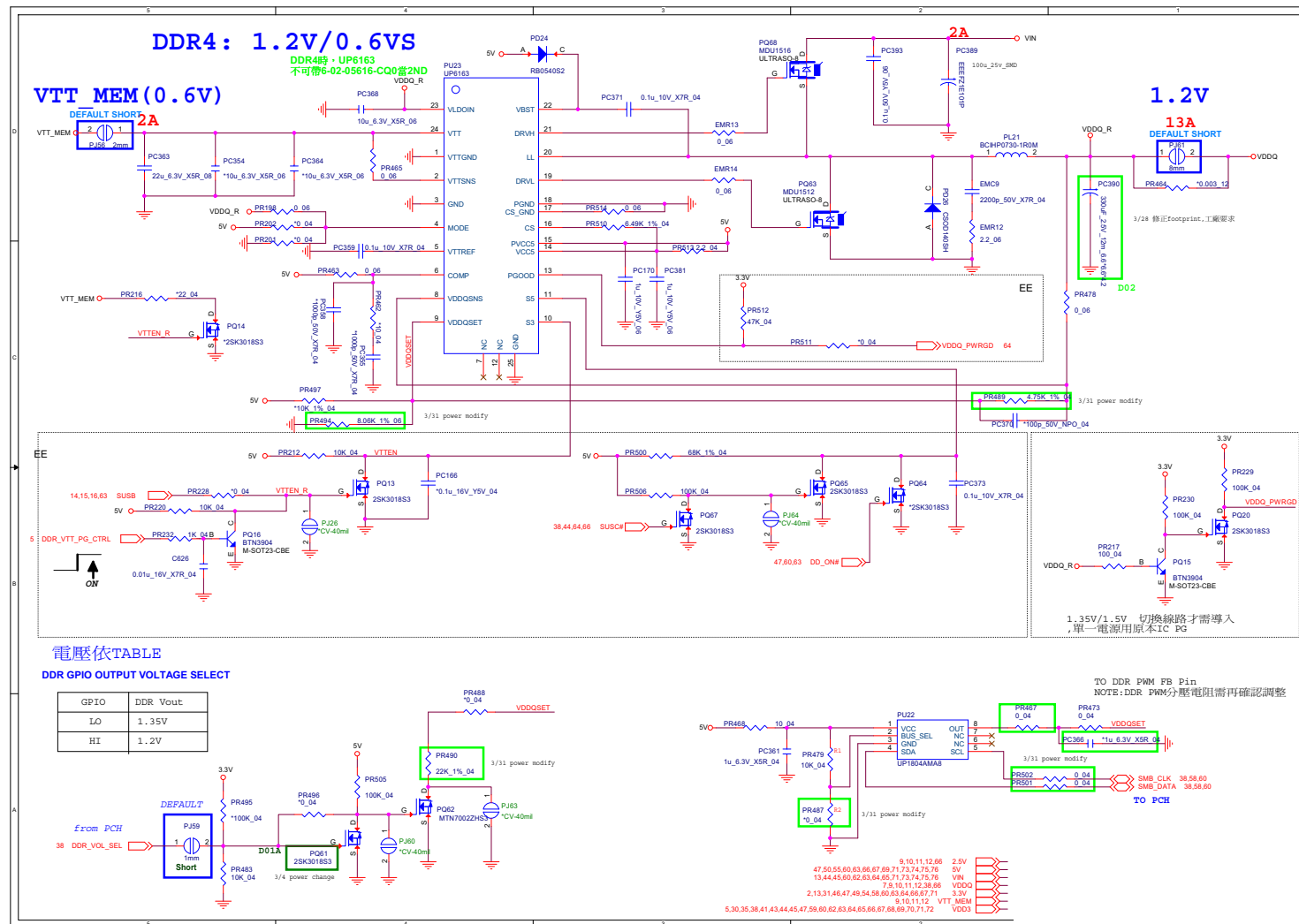


Connector

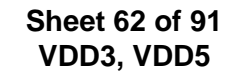
Sheet 60 of 91
Connector

DDR 1.2V / 0.6V

Sheet 61 of 91
DDR 1.2V / 0.6VS



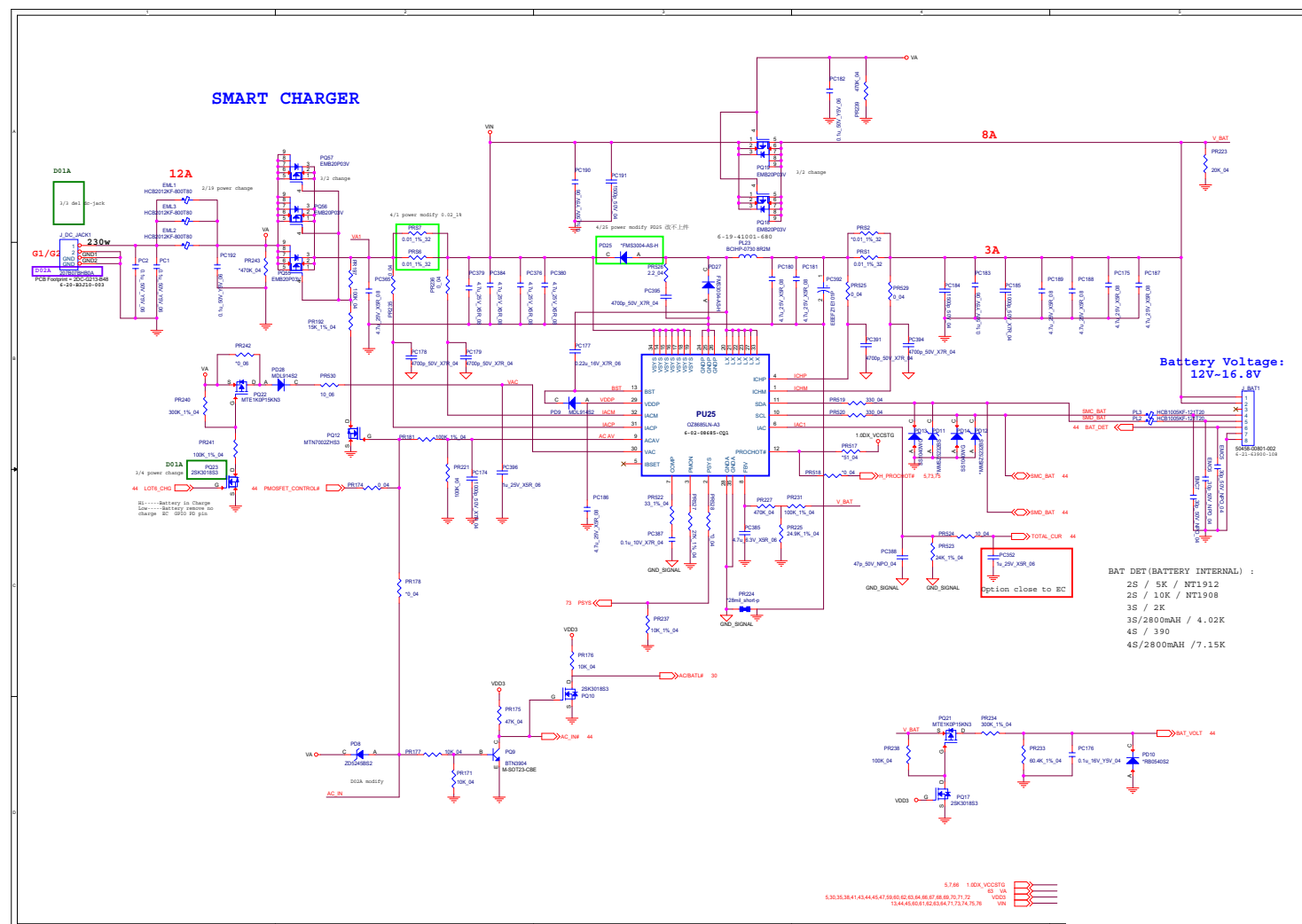
VDD3, VDD5 B - 63



AC_In, Charger

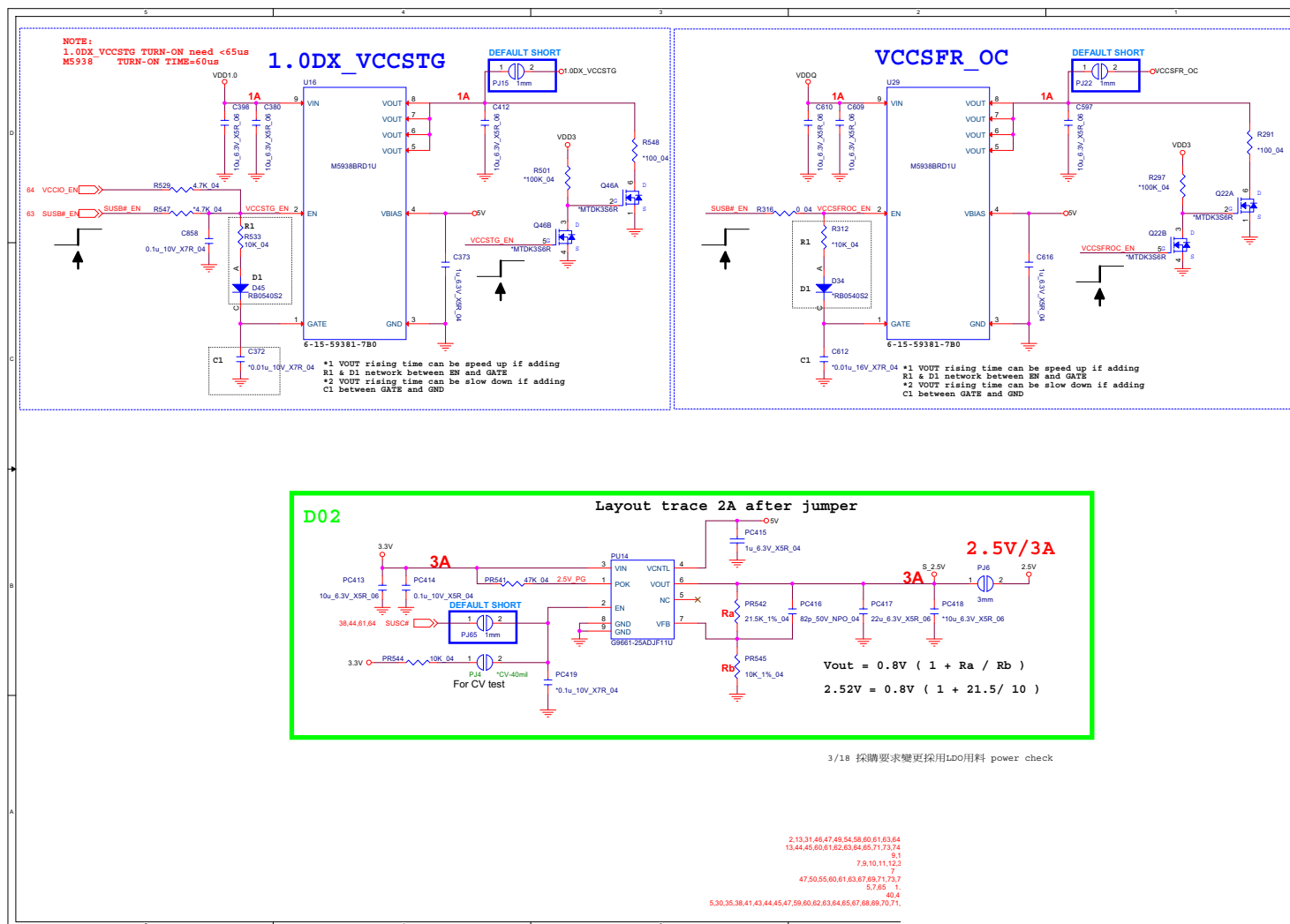
B. Schematic Diagrams

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AC_In, Charger



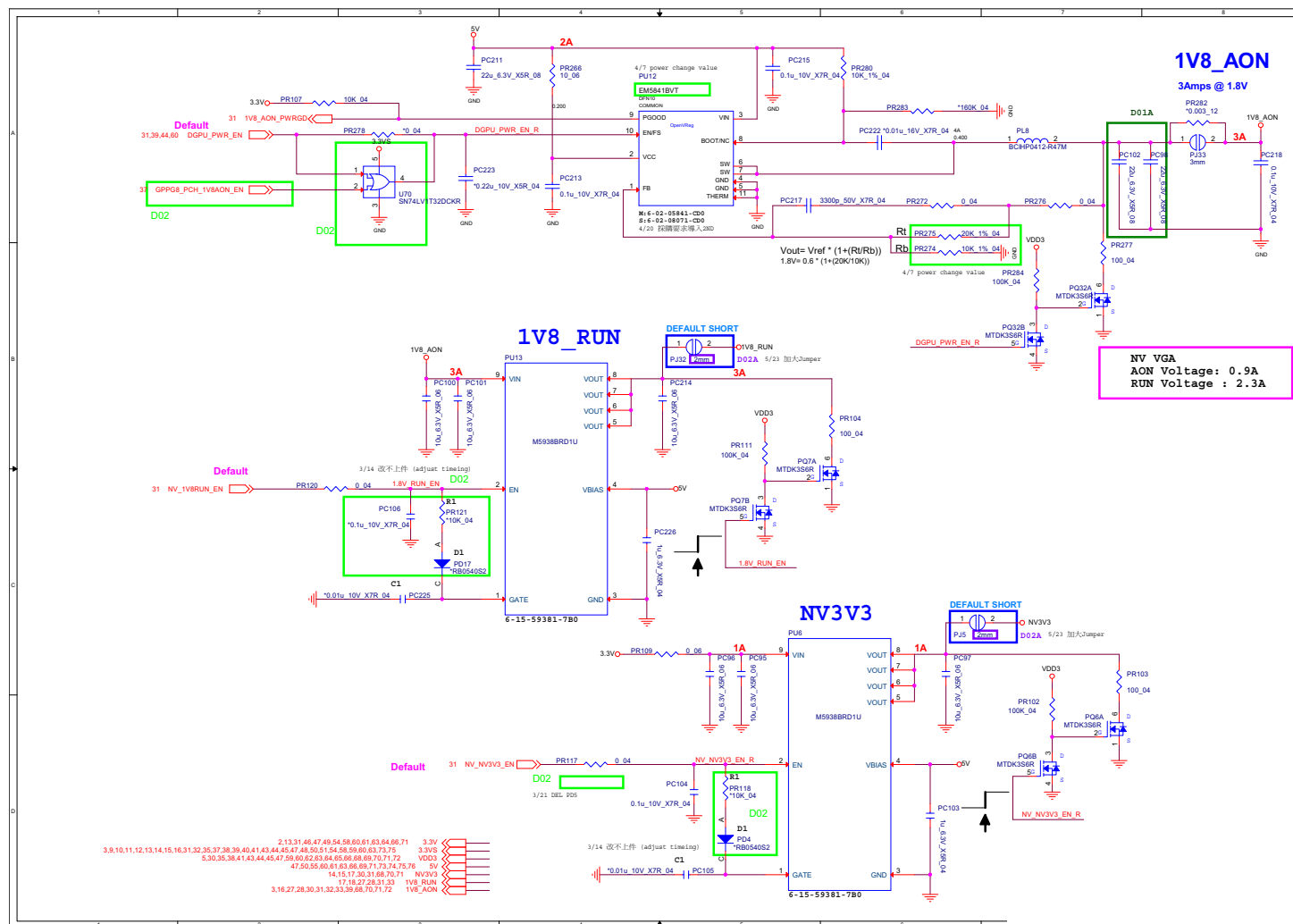
1.0DX_VCCSTG/VCCSFR_OC/2.5V

Sheet 66 of 91
1.0DX_VCCSTG/
VCCSFR_OC/2.5V



B.Schematic Diagrams

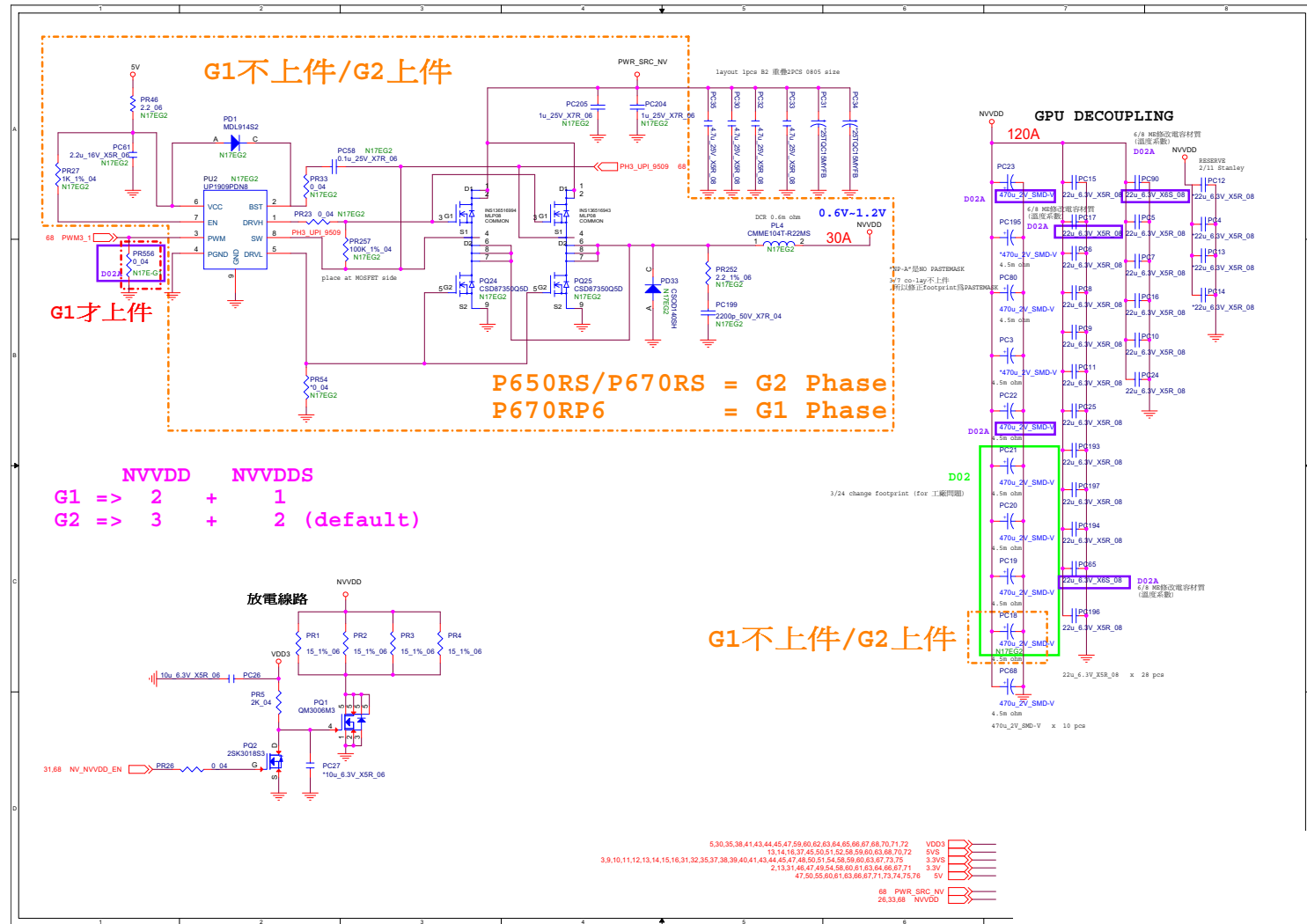
Sheet 67 of 91_Run
1V8_RUN/AON,
NV3V3



Schematic Diagrams

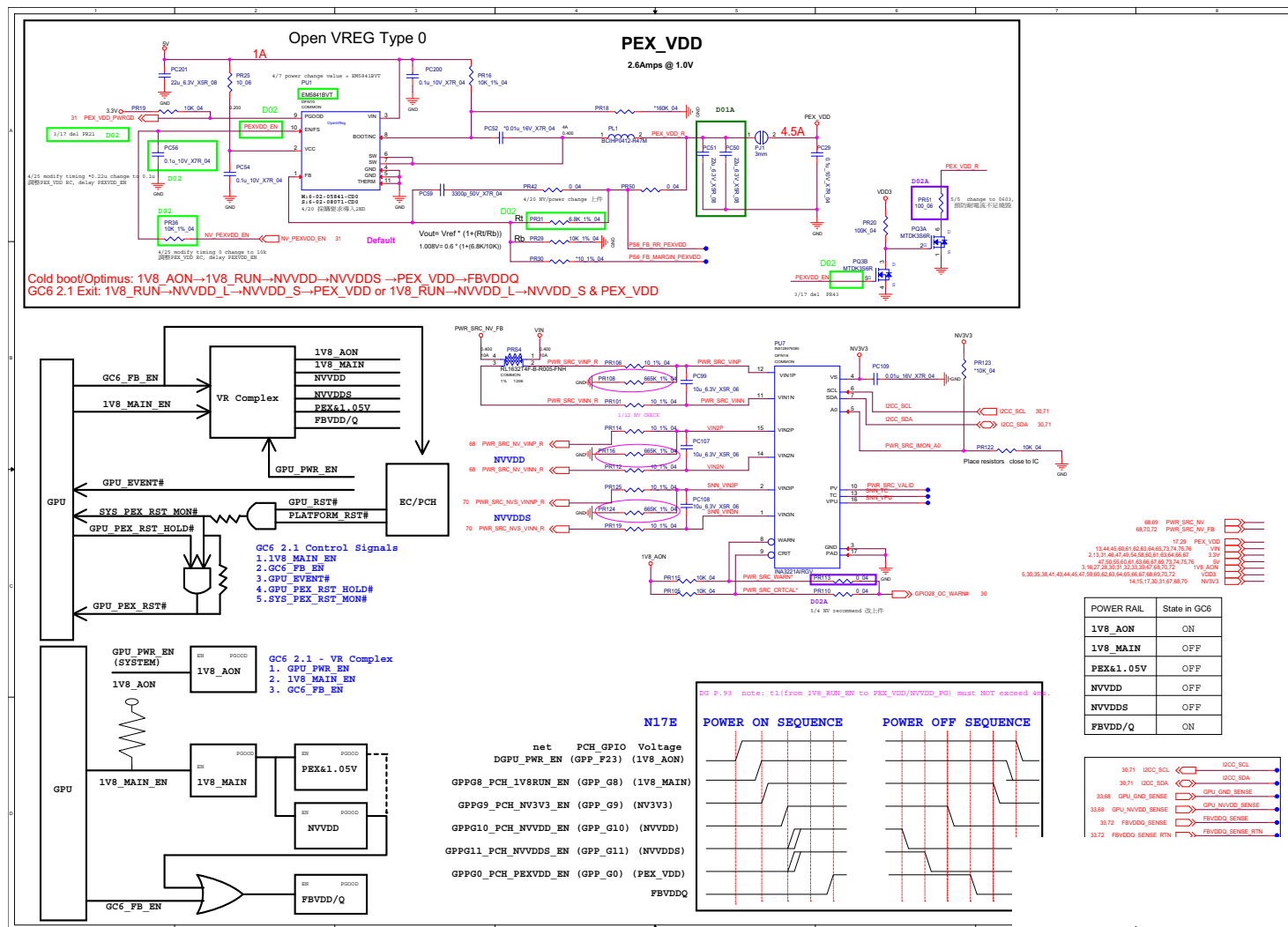
NVVDD Phase 3~4

Sheet 69 of 91
NVVDD Phase 3~4

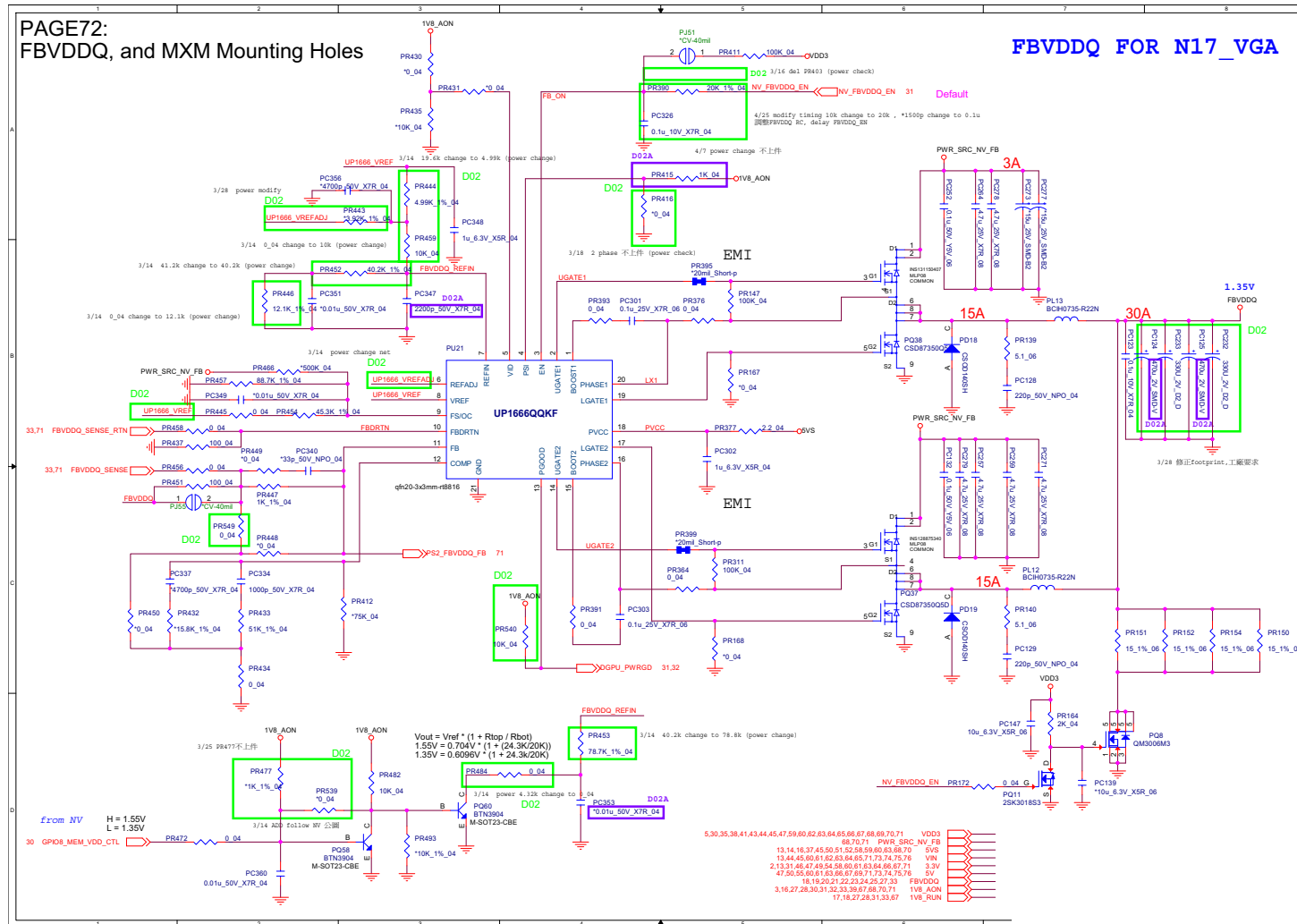


PEX_VDD

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PEX_VDD



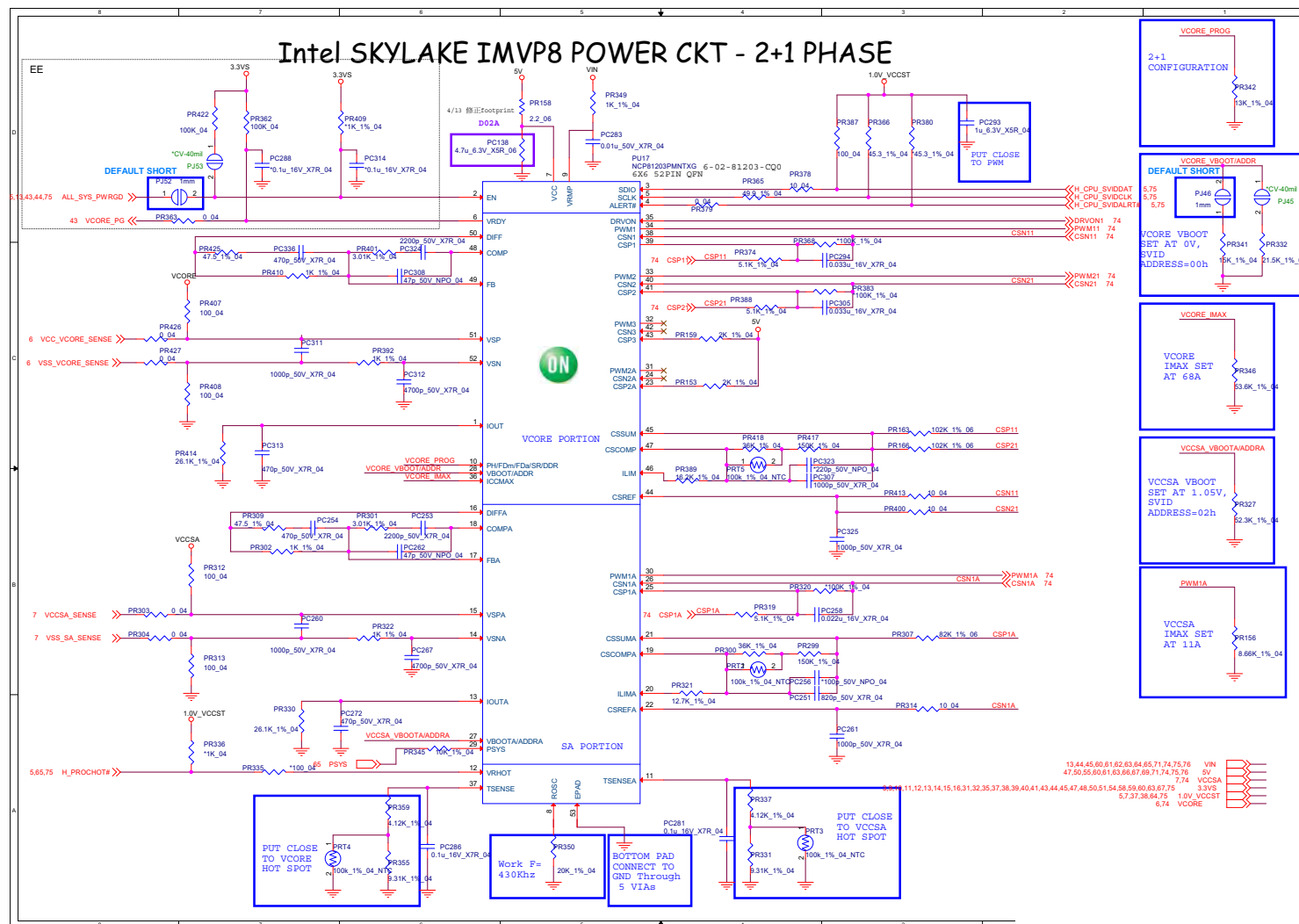
FBVDDQ B - 73



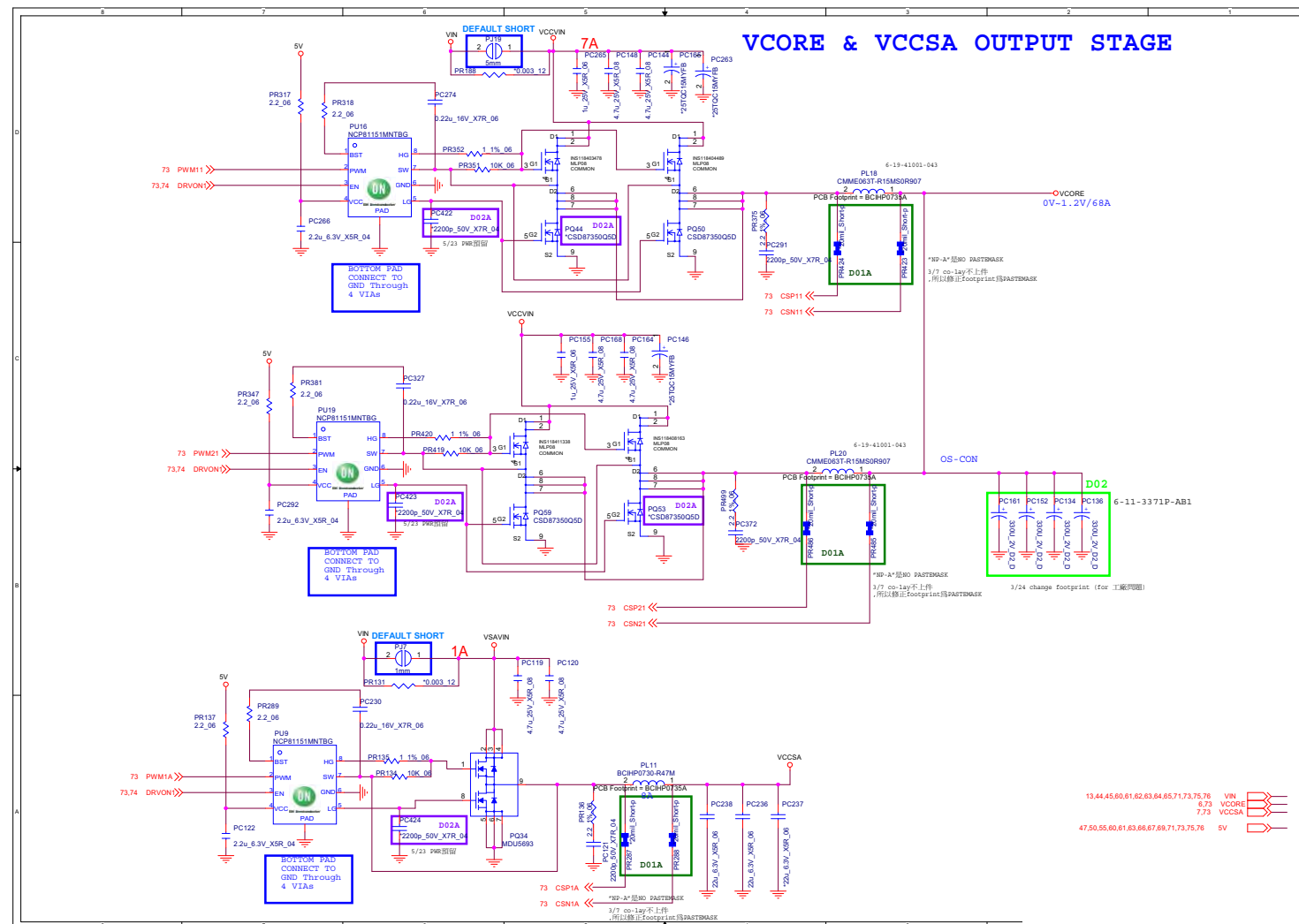
VCC_Core & VCCSA

B. Schematic Diagrams

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VCC_Core &
VCCSA



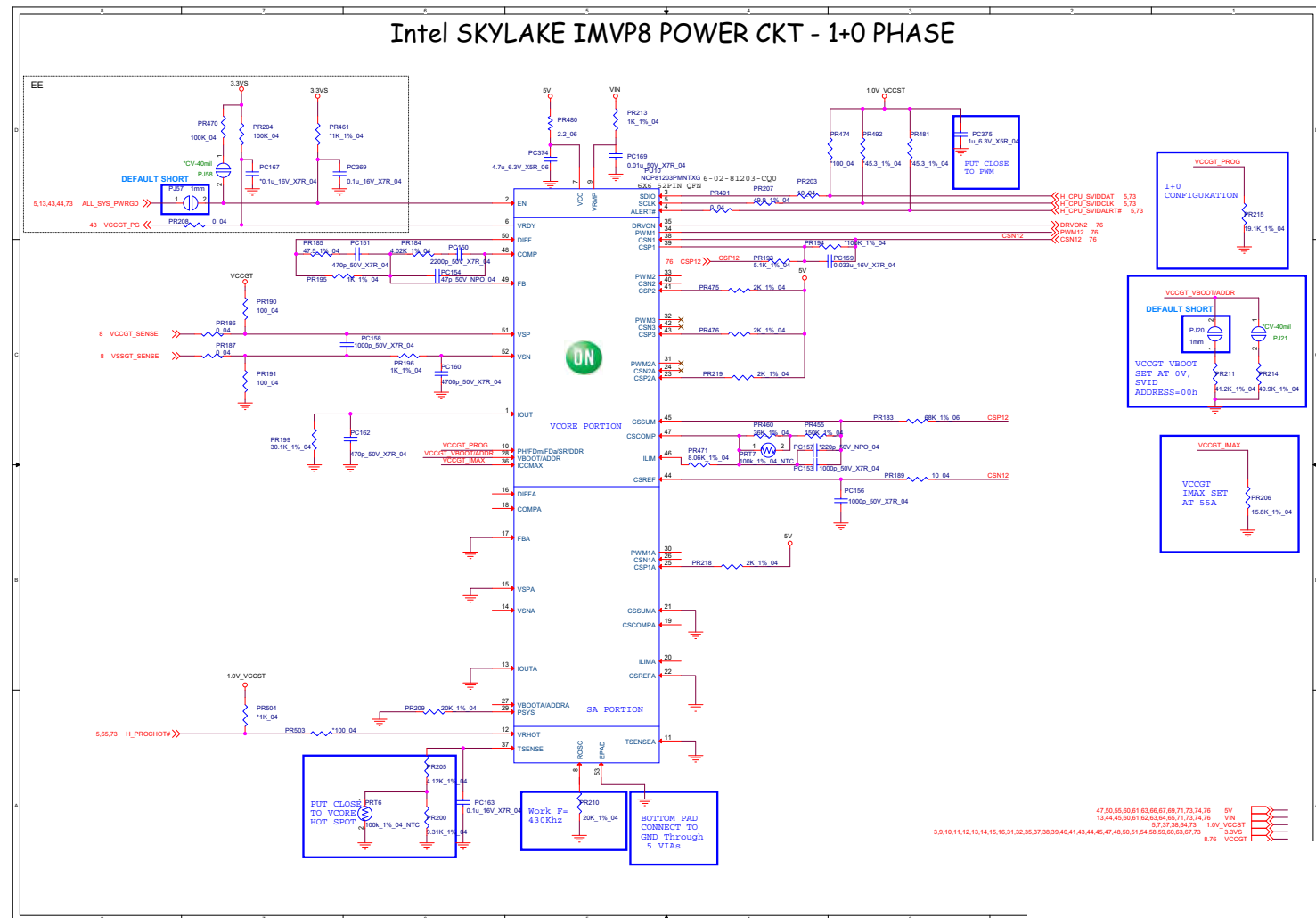
VCore Output Stage



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VCore Output
Stage

VCCGT

B. Schematic Diagrams



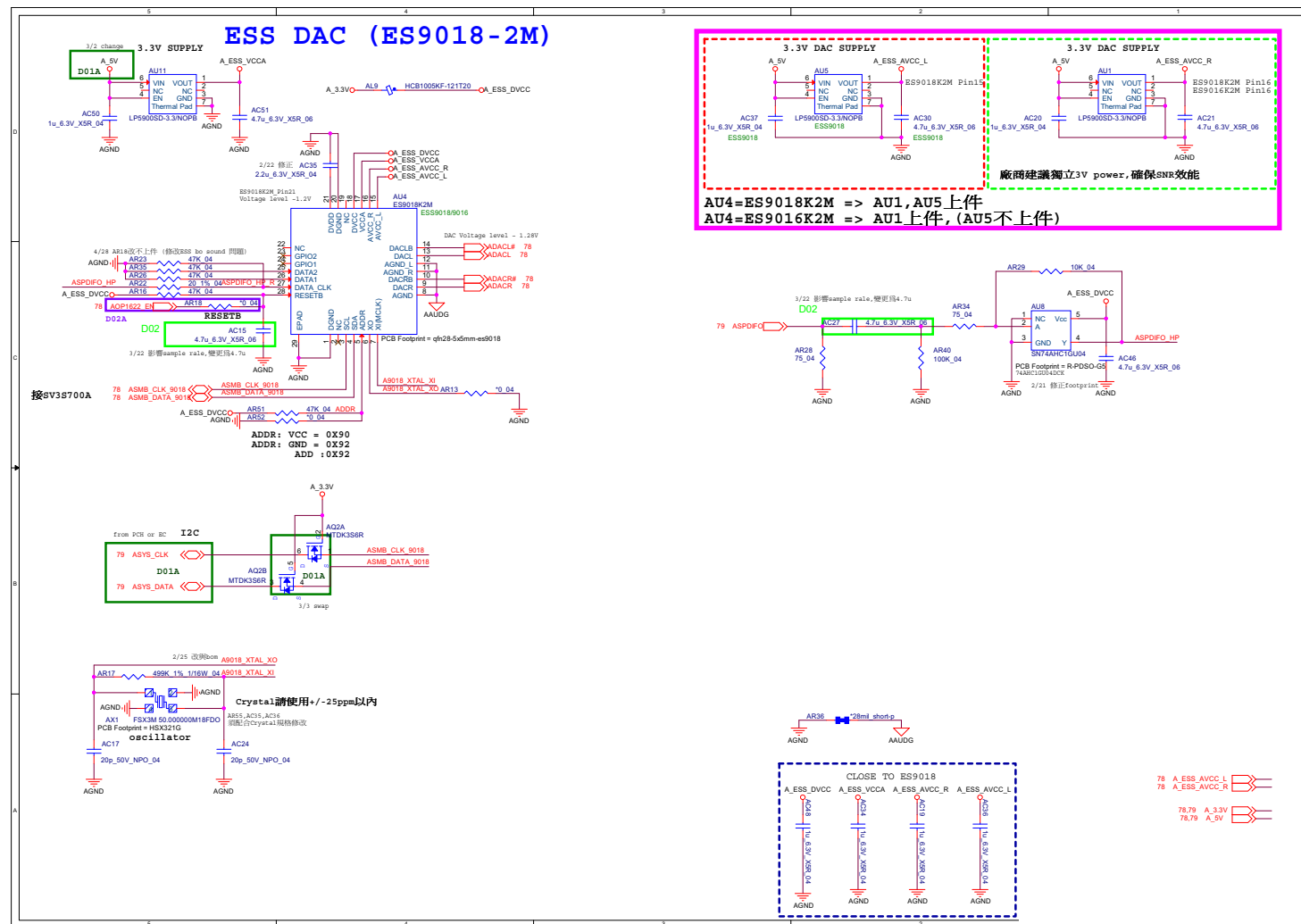
PCB layout diagram for a motor driver circuit. The circuit includes a 5V input, a PWM signal (PWM12), a DRVON2 signal, and a VCCGT output. Key components include a MOSFET (NCP81151MNTBG), a diode (D02A), and various capacitors (PC173, PC171, PC383, PC378, PC367, PC425, PC521, PC386, PC382, PC172, PC141, PC142, PC143). The layout shows the placement of these components on a PCB with dimensions and annotations. A note indicates that the footprint for the diode (D02A) should be corrected to match the manufacturer's requirements.

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VCCGT Output
Stage

Audio Board P65_ESS_A 1/3

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Audio Board
P65_ESS_A 1/3

B. Schematic Diagrams

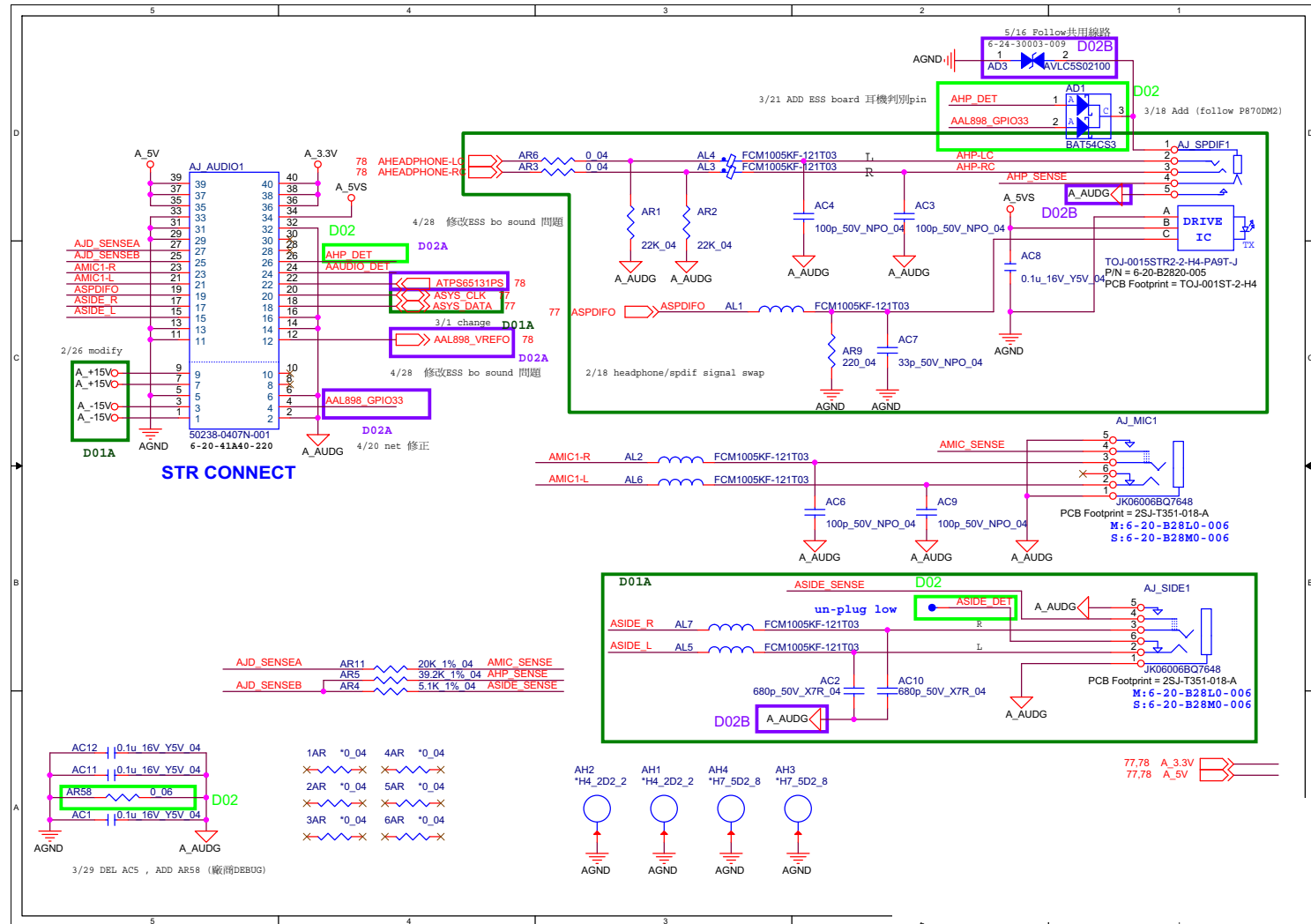


Sheet 78 of 91
Audio Board
P65 ESS A 2/3

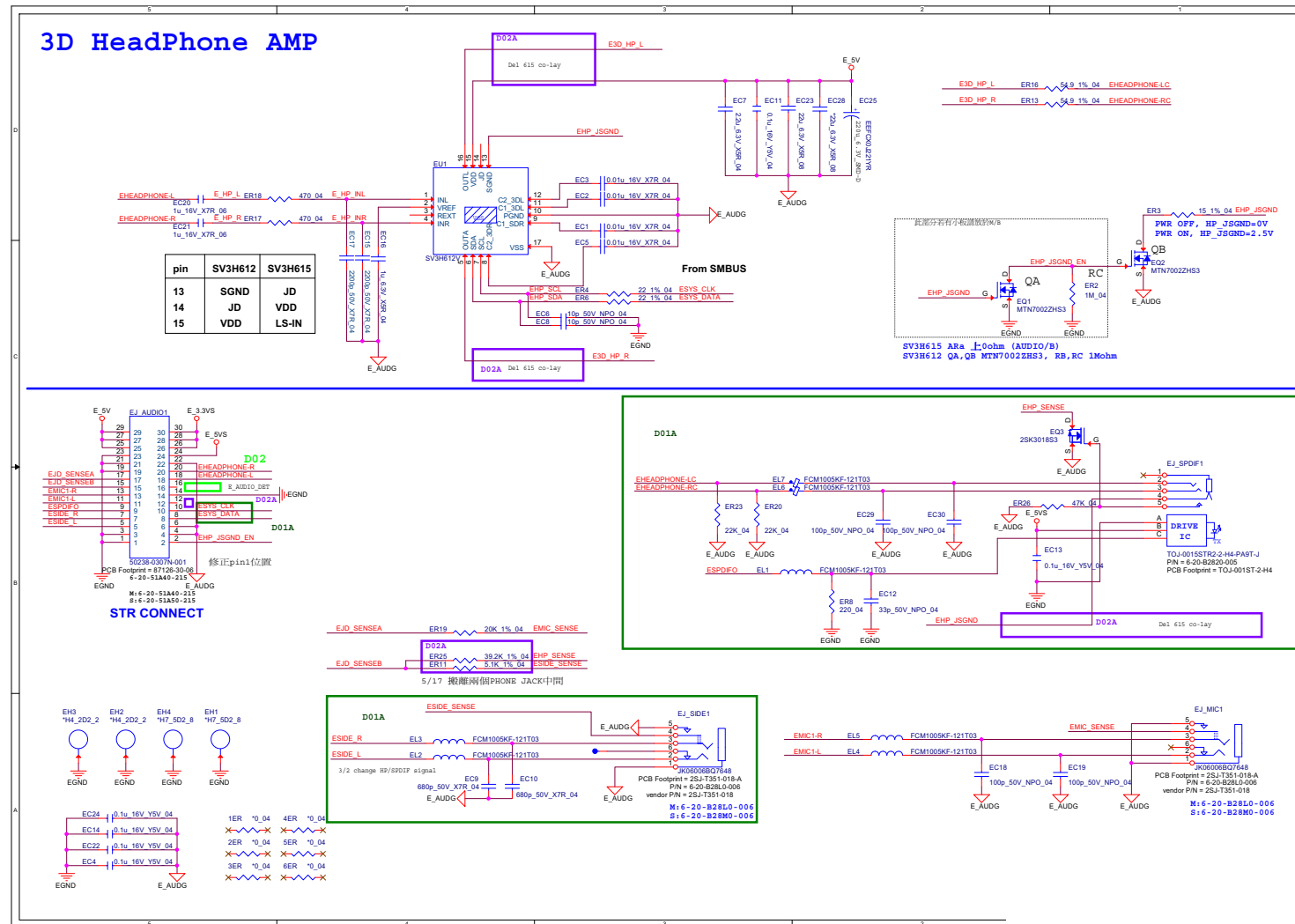


Schematic Diagrams

Audio Board P65_ESS_A 3/3



Audio Board P67_3DAMP_E

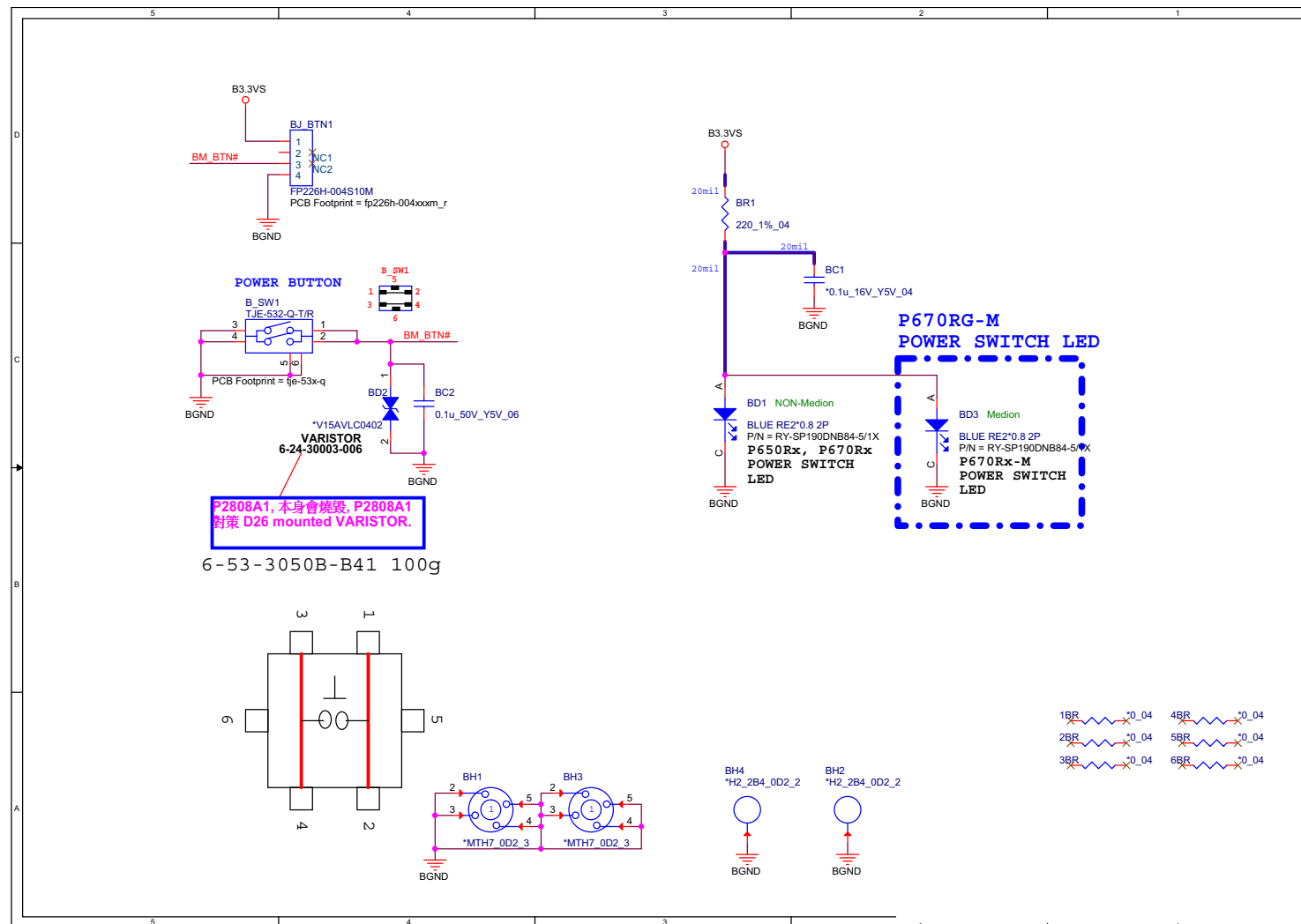


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Audio Board
P67_3DAMP_E

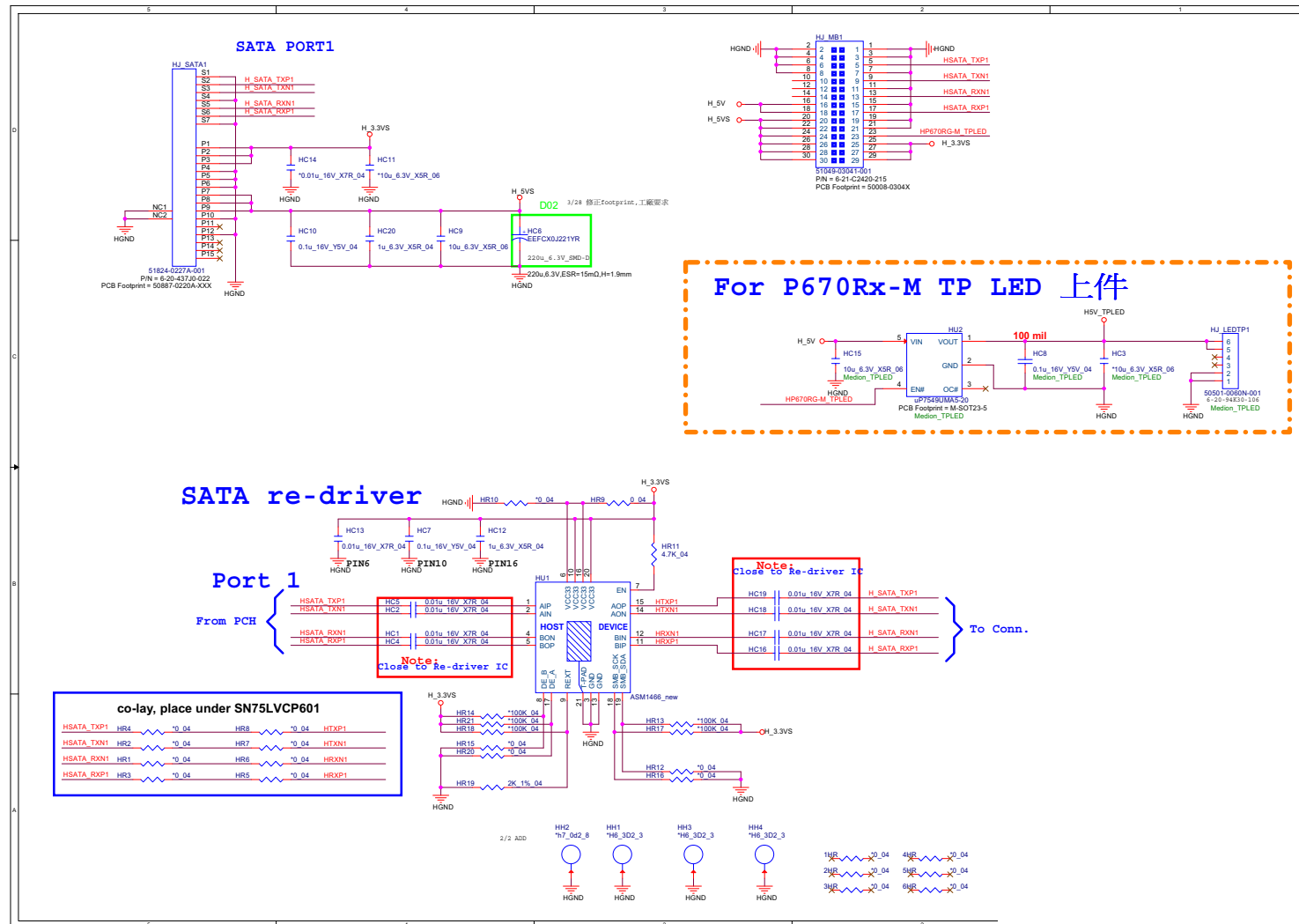
Schematic Diagrams

P650RS Power Board

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P650RS Power
Board



P650RS HDD Board

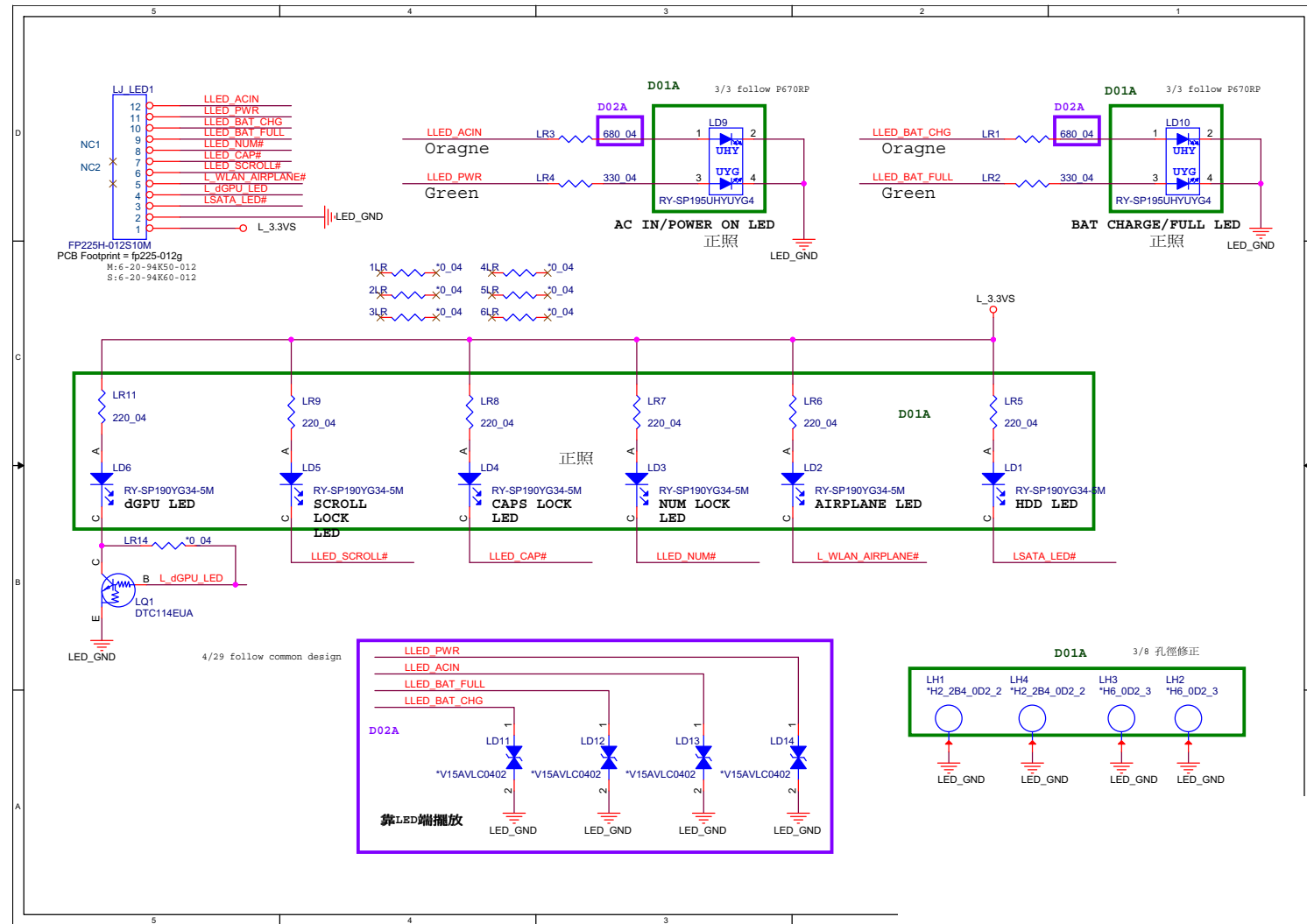


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P650RS HDD Board

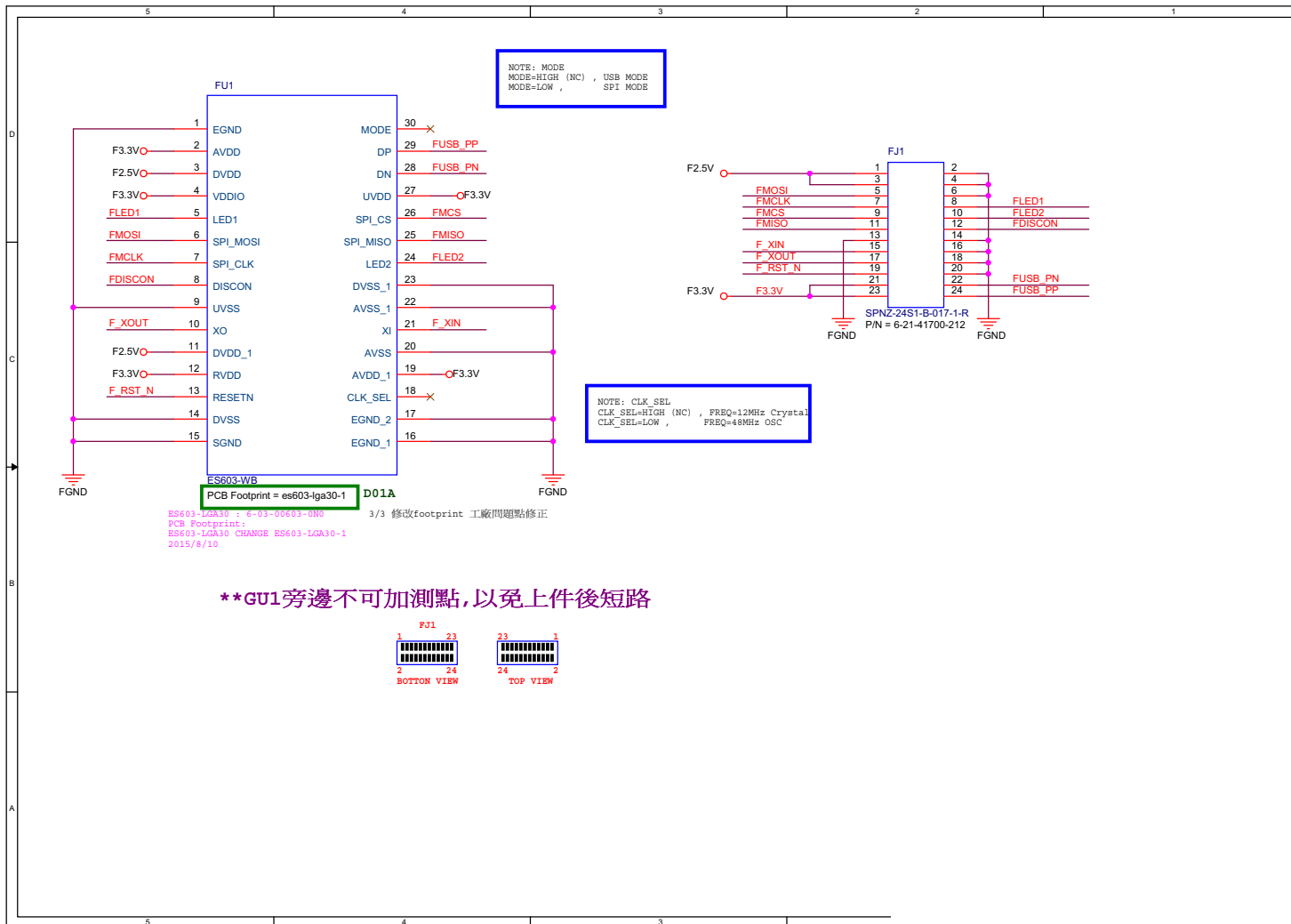
Schematic Diagrams

P650RS LED Board

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P650RS LED Board



P650RS FP Board

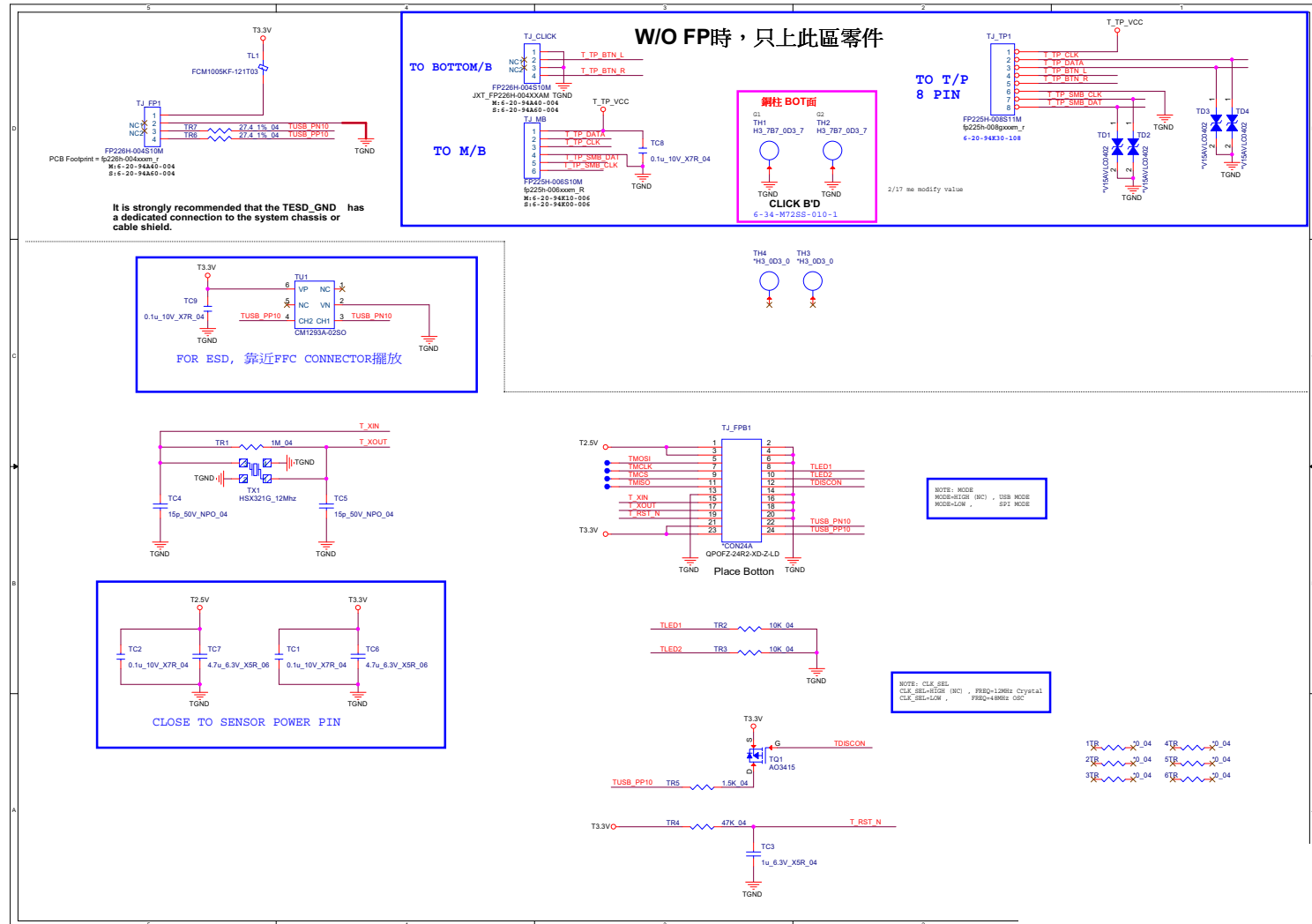


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P650RS FP Board

Schematic Diagrams

P650RS Click Board

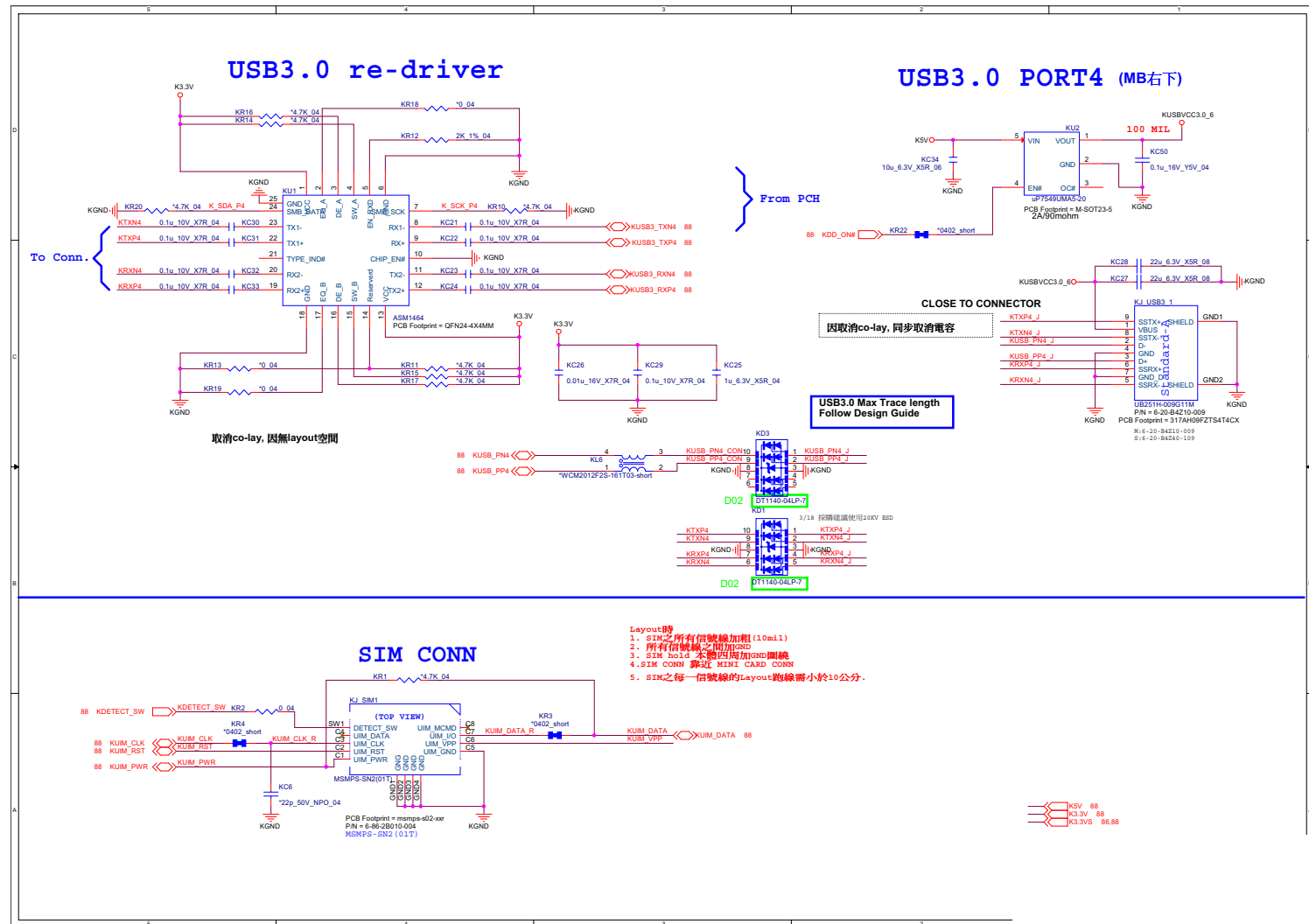
Sheet 85 of 91
P650RS Click
Board



Schematic Diagrams

P650RS USB Board 2/3

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P650RS USB Board
2/3



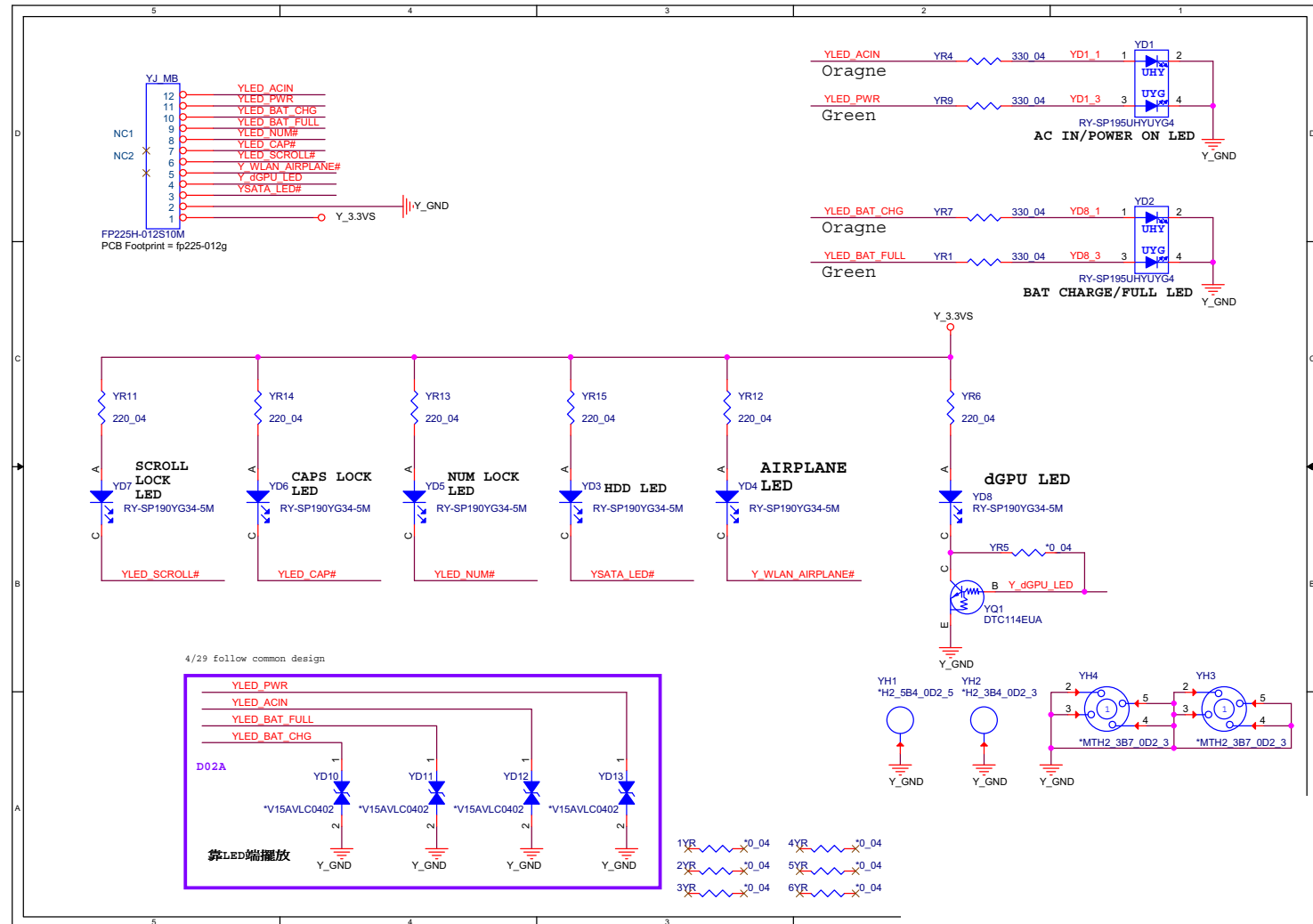
[illegible]

P650RS USB Board 3/3 B - 89

Schematic Diagrams

P670RS LED Board

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P670RS LED Board



Sheet 90 of 91
P670RS USB Board
1/2



